

Figure 1A ( Prior ART )

2 of 51

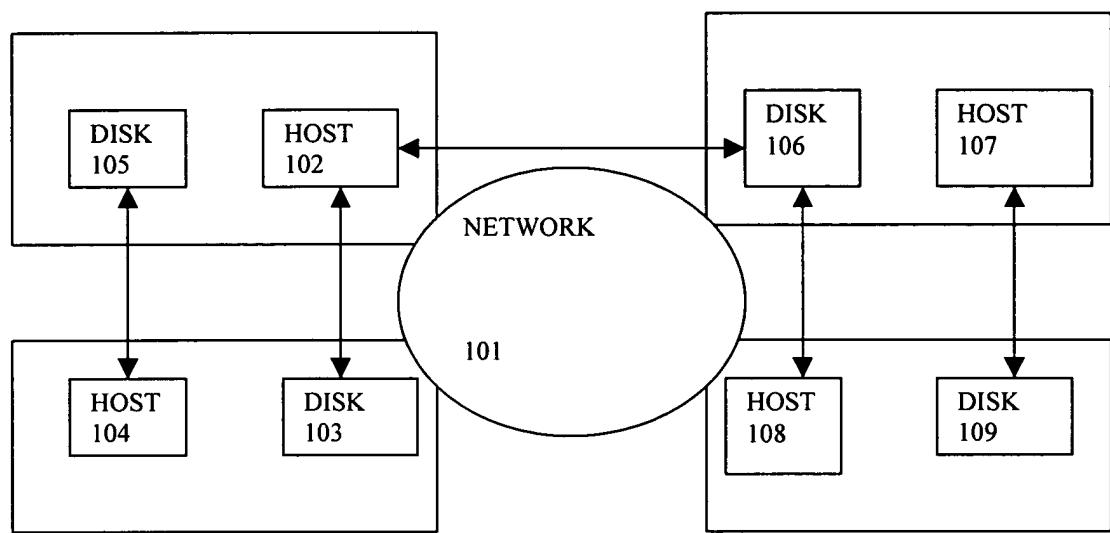


FIGURE 2A

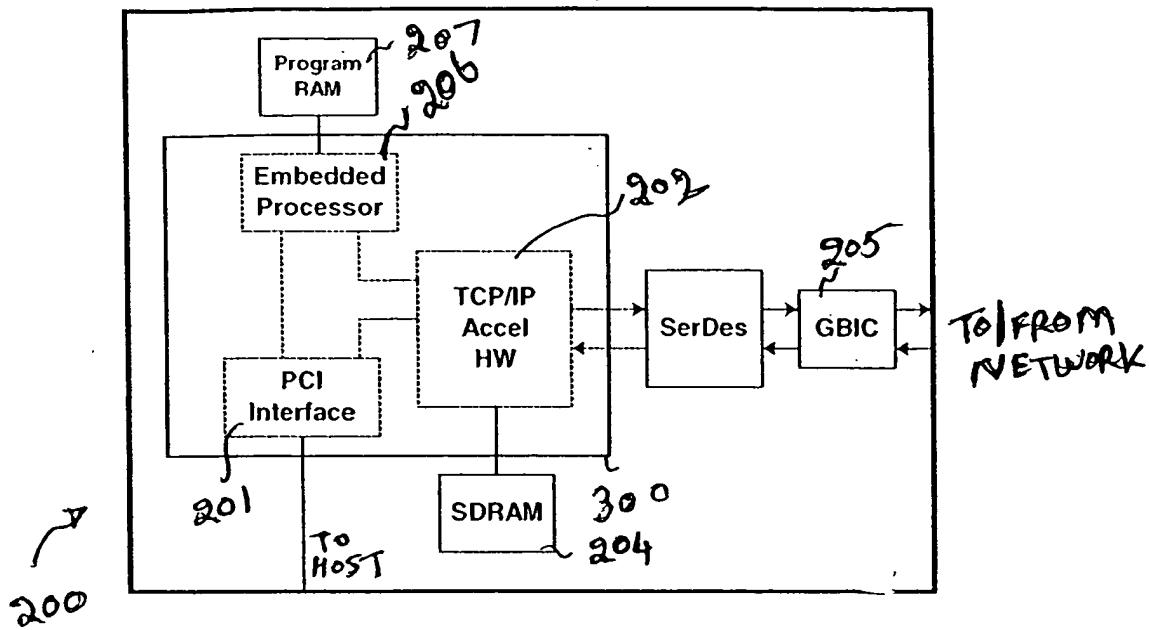


Figure 2B

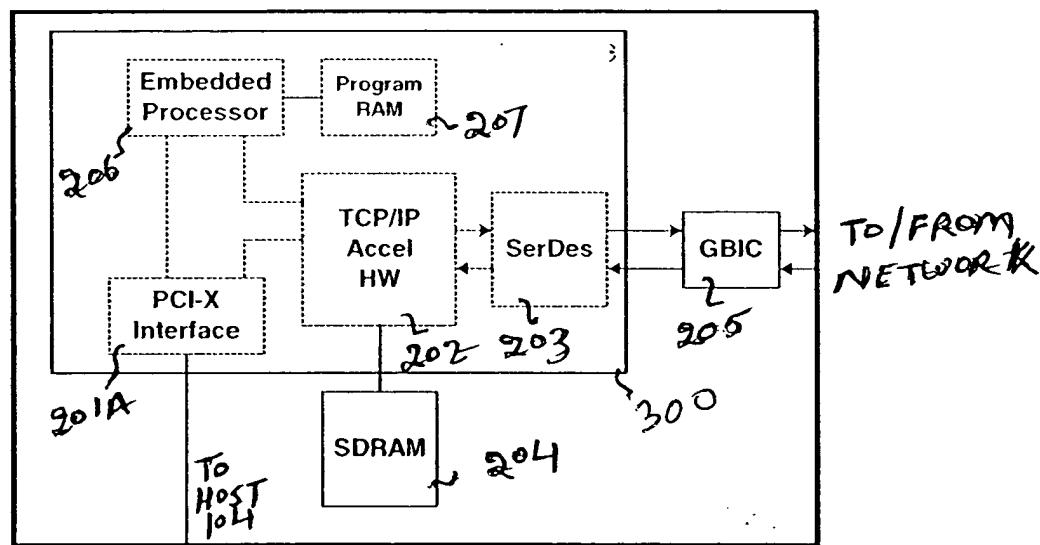


Figure 2C

200A

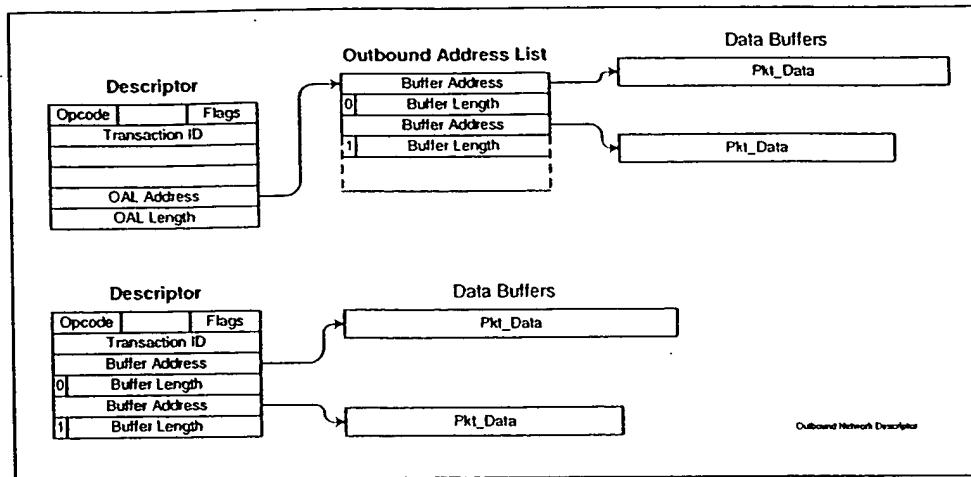


Figure 3A-1 Network IOCB Example

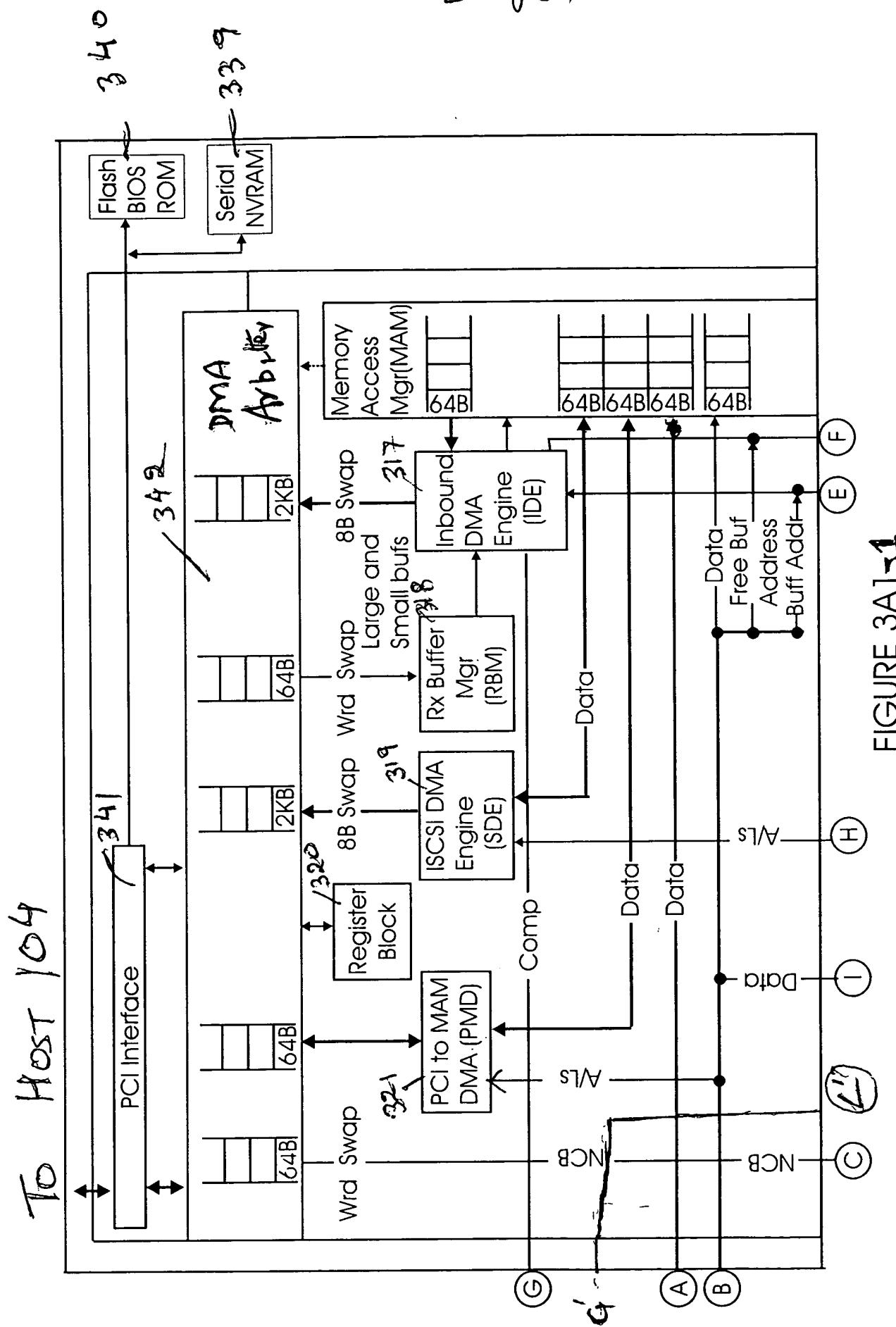
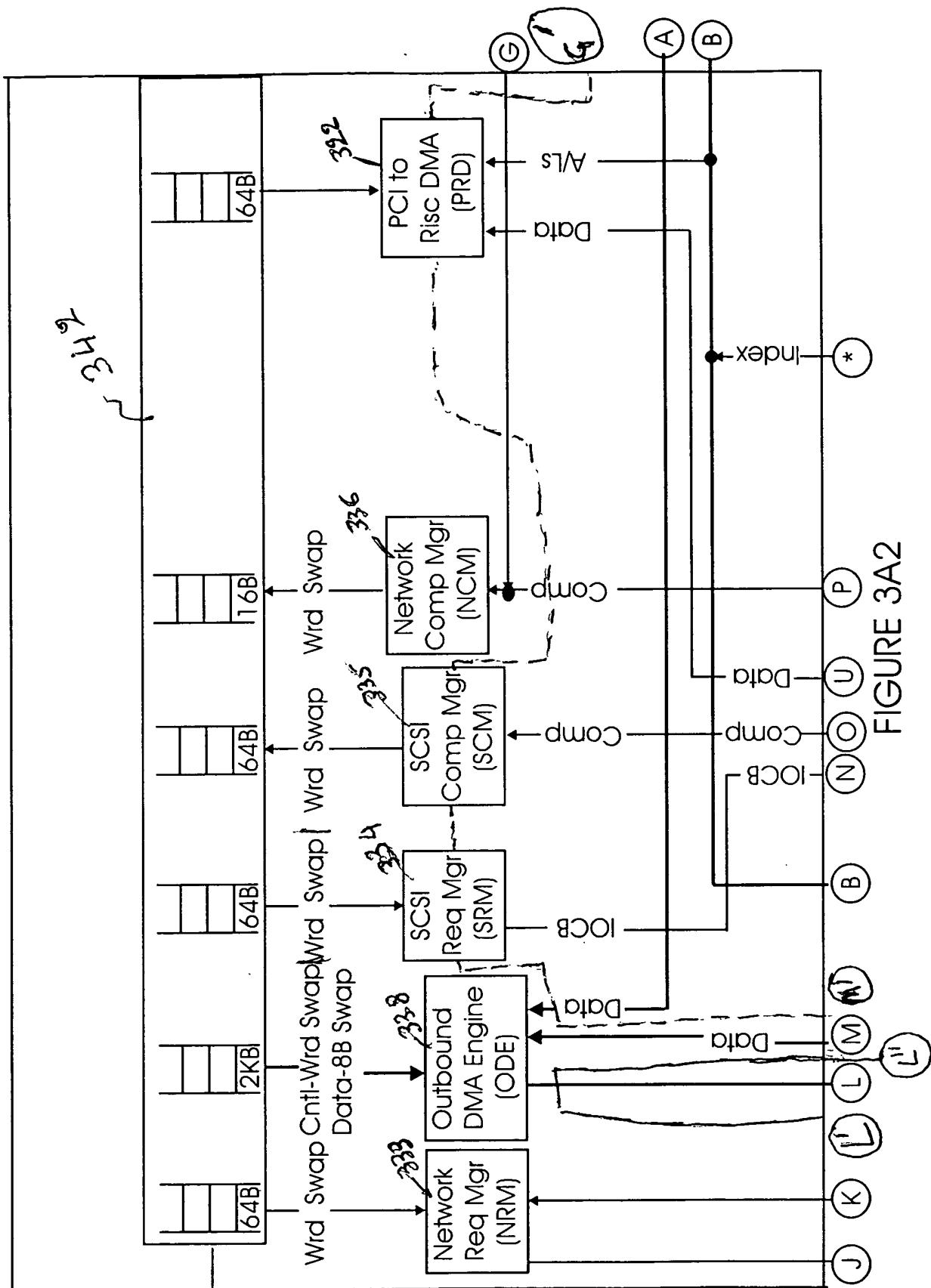
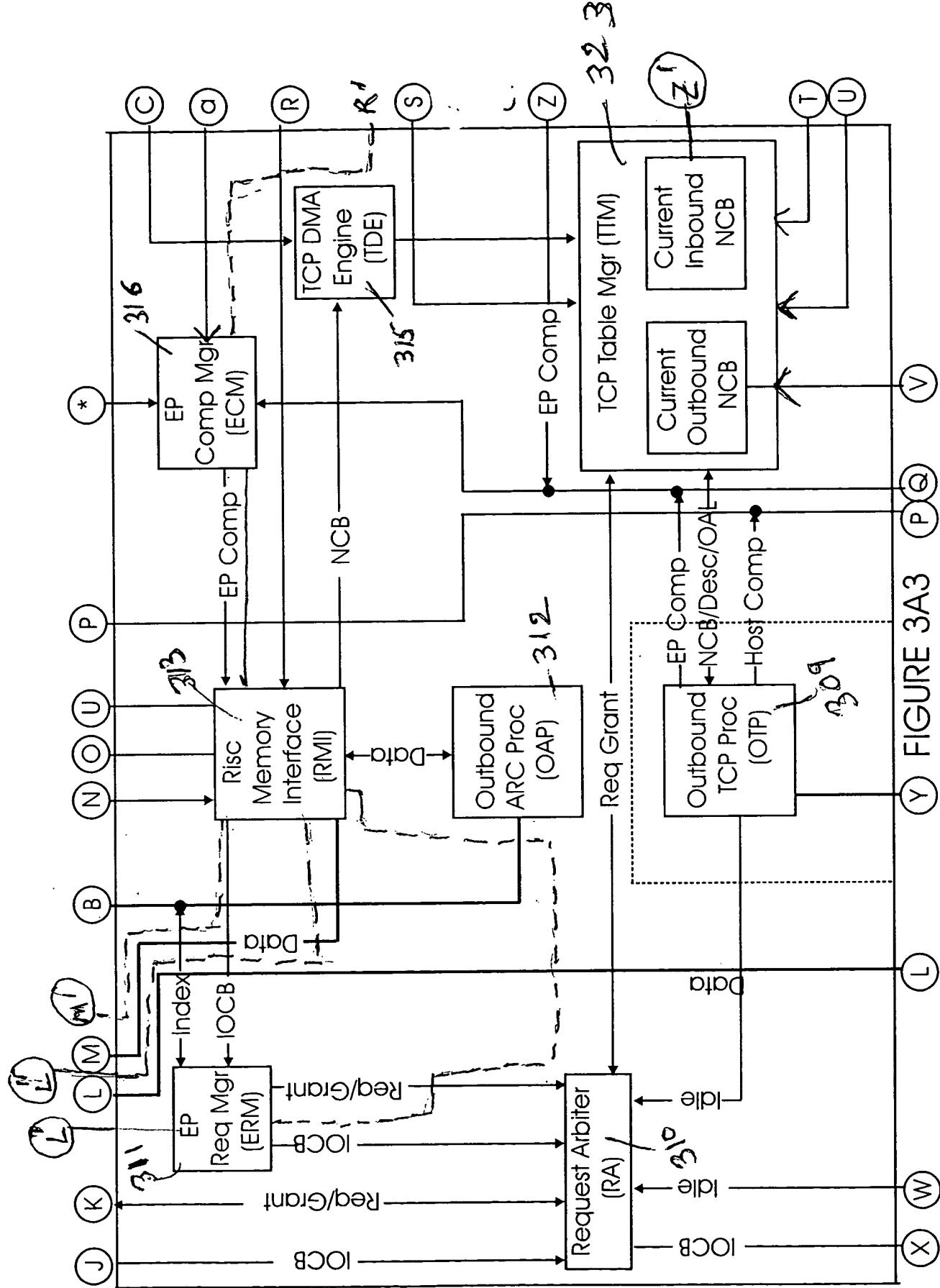


FIGURE 3A1-1





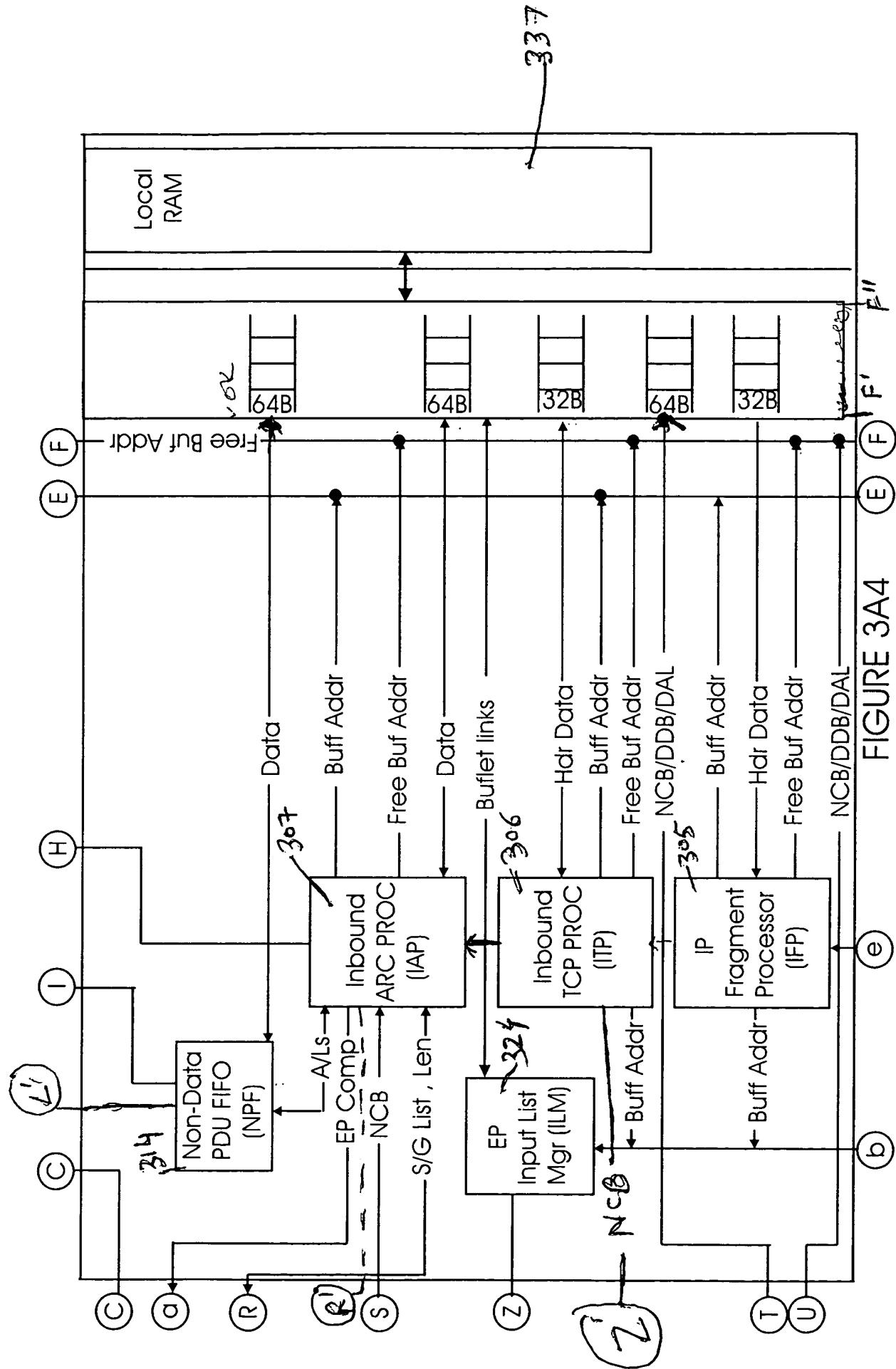
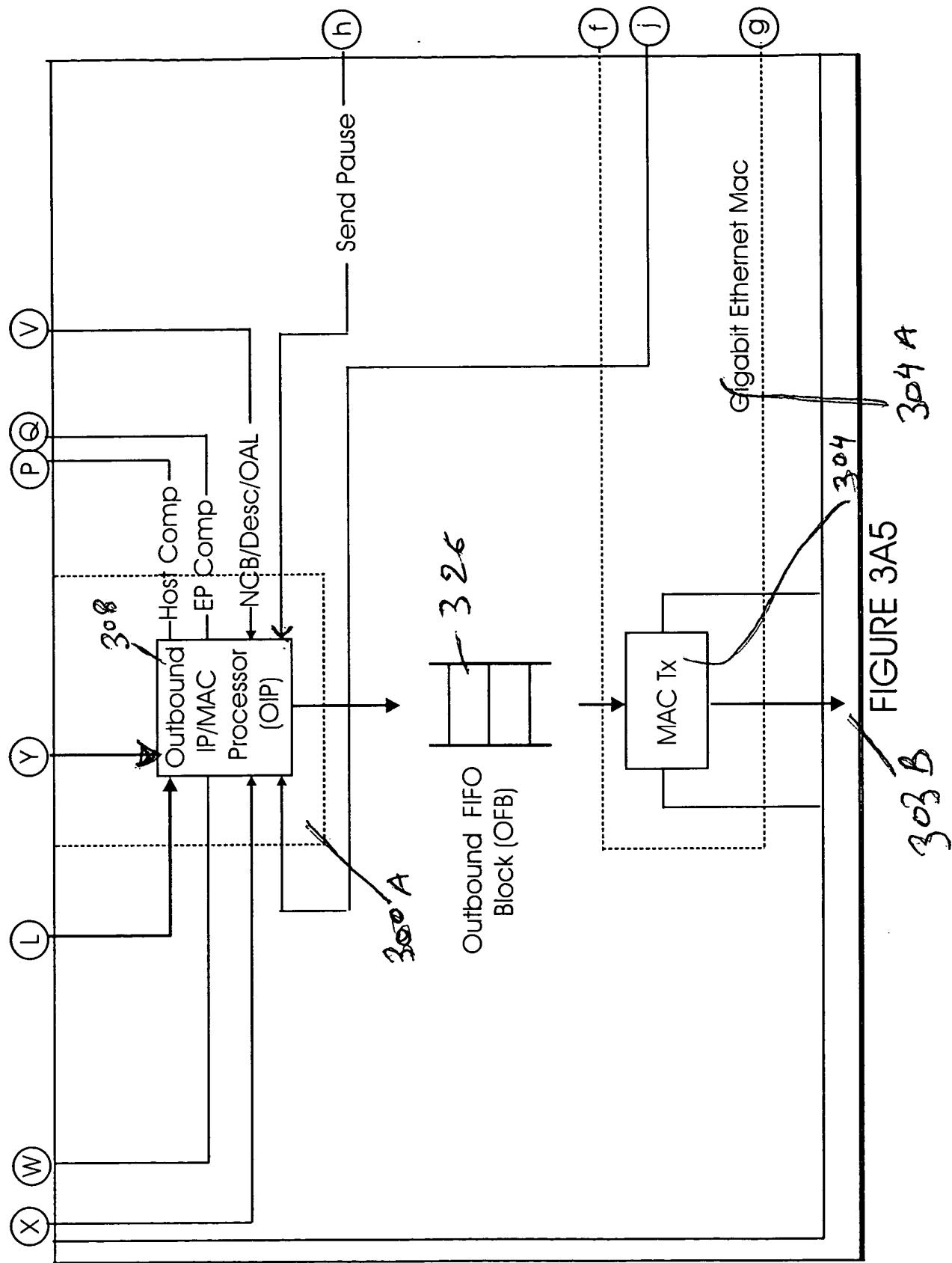
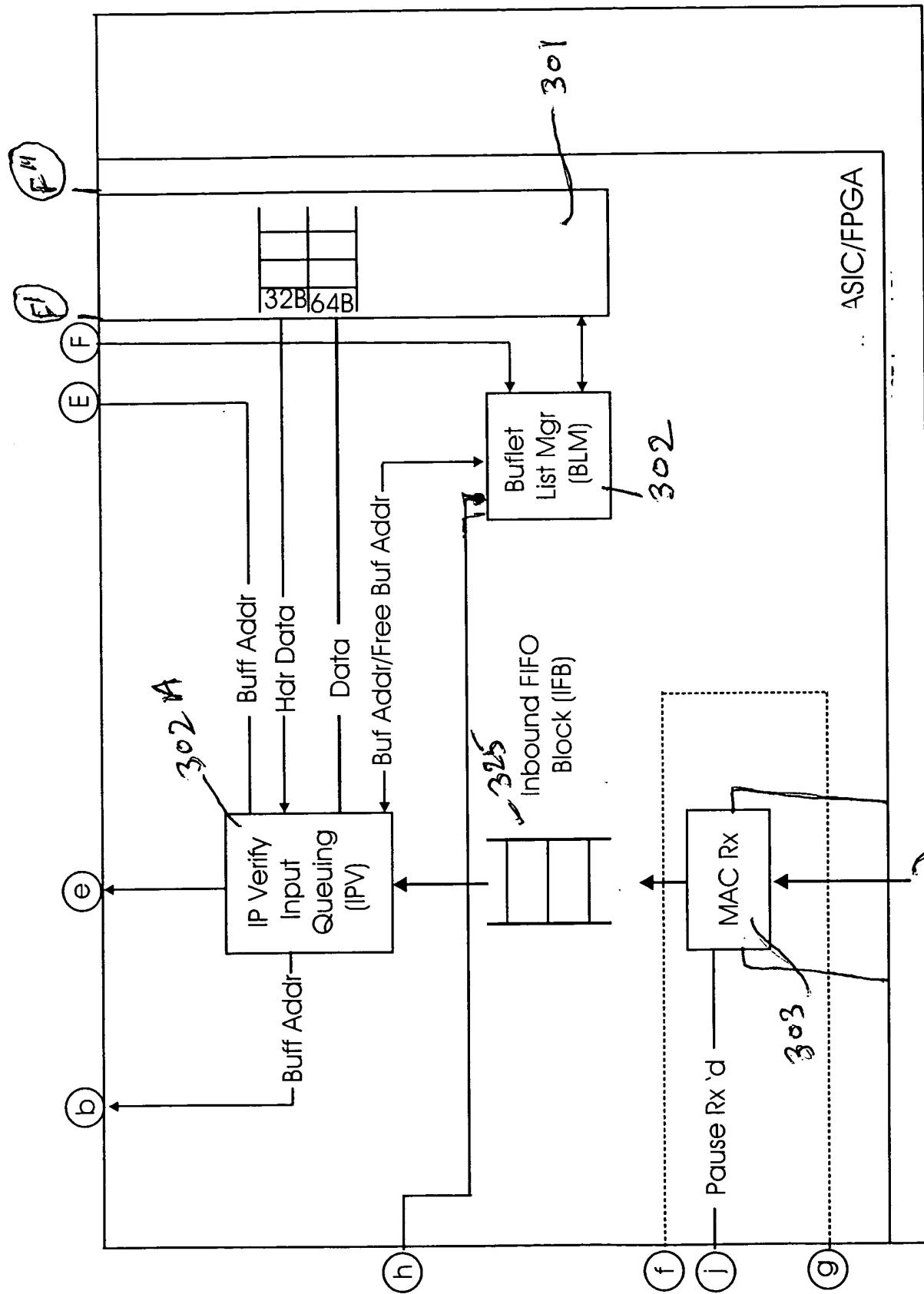


FIGURE 3A4





331 FIGURE 3A6

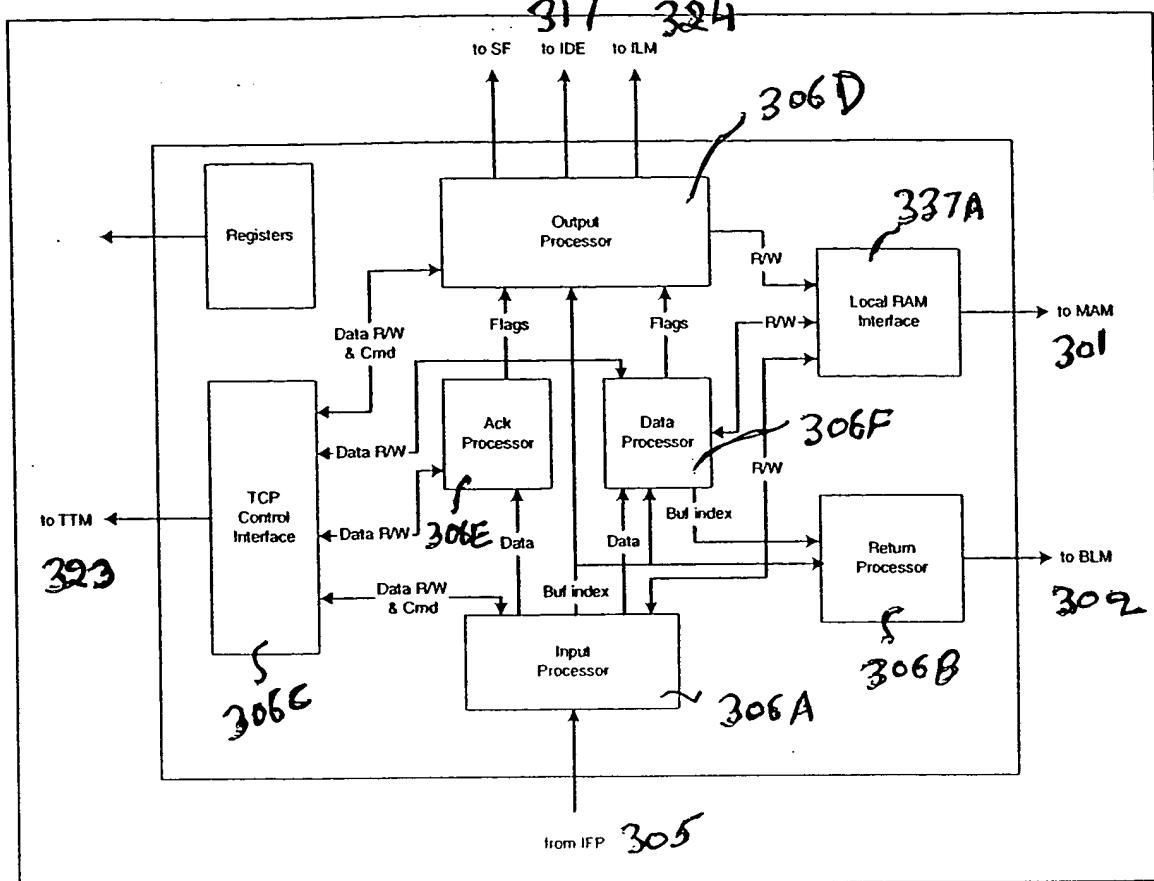


Figure 3B -

306

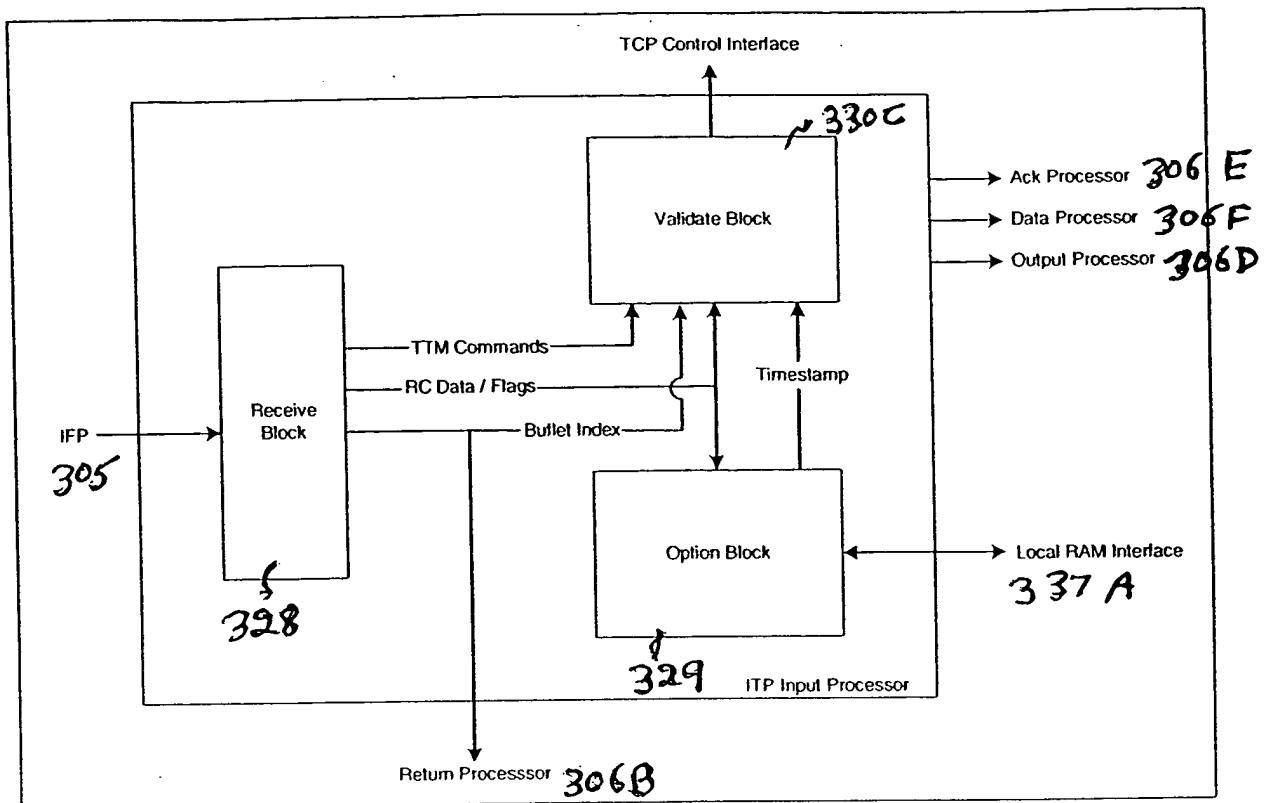


Figure 3C – JTP Input Processor

306A

306B

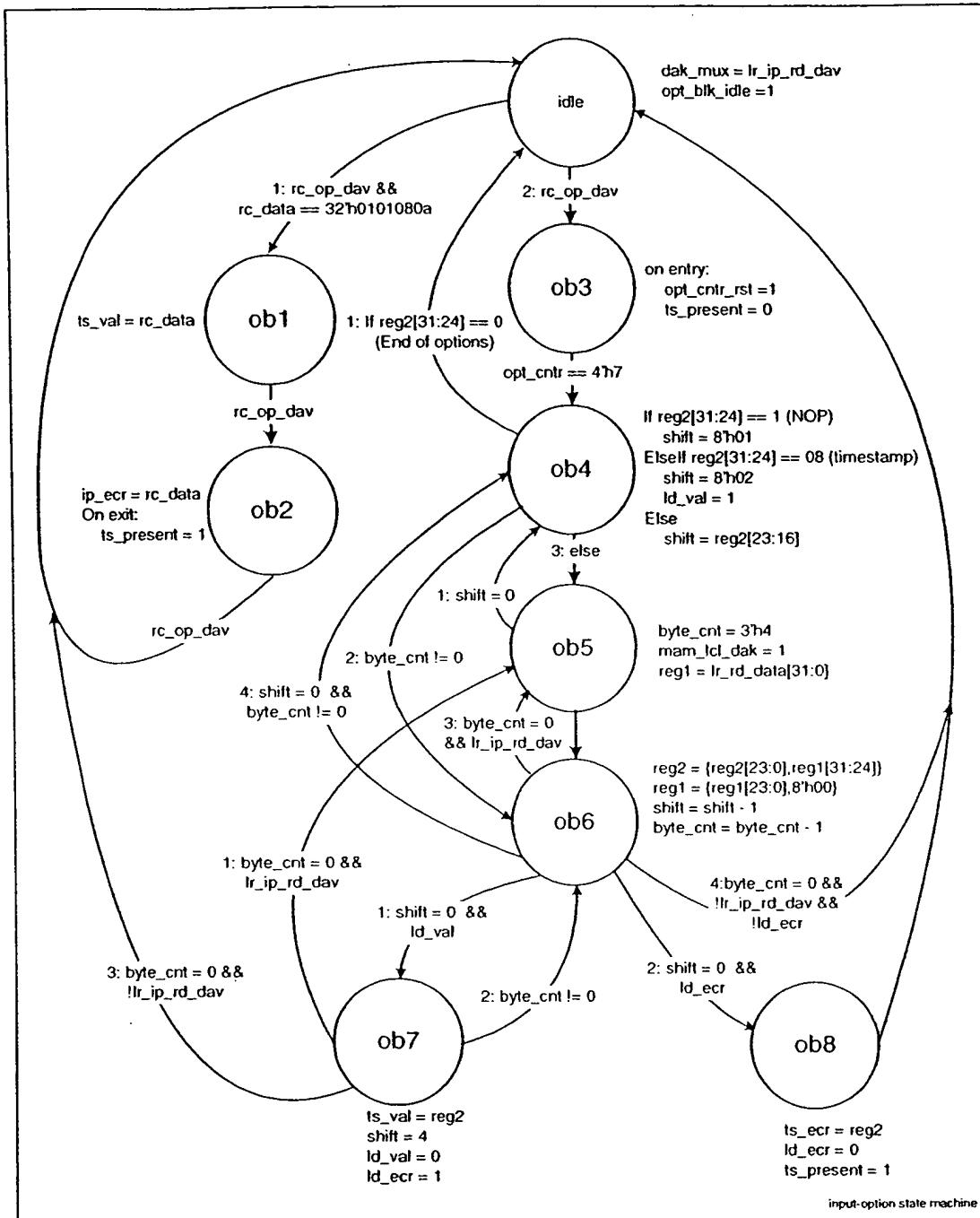


Figure 3C1 Input Processor Option Block State Machine

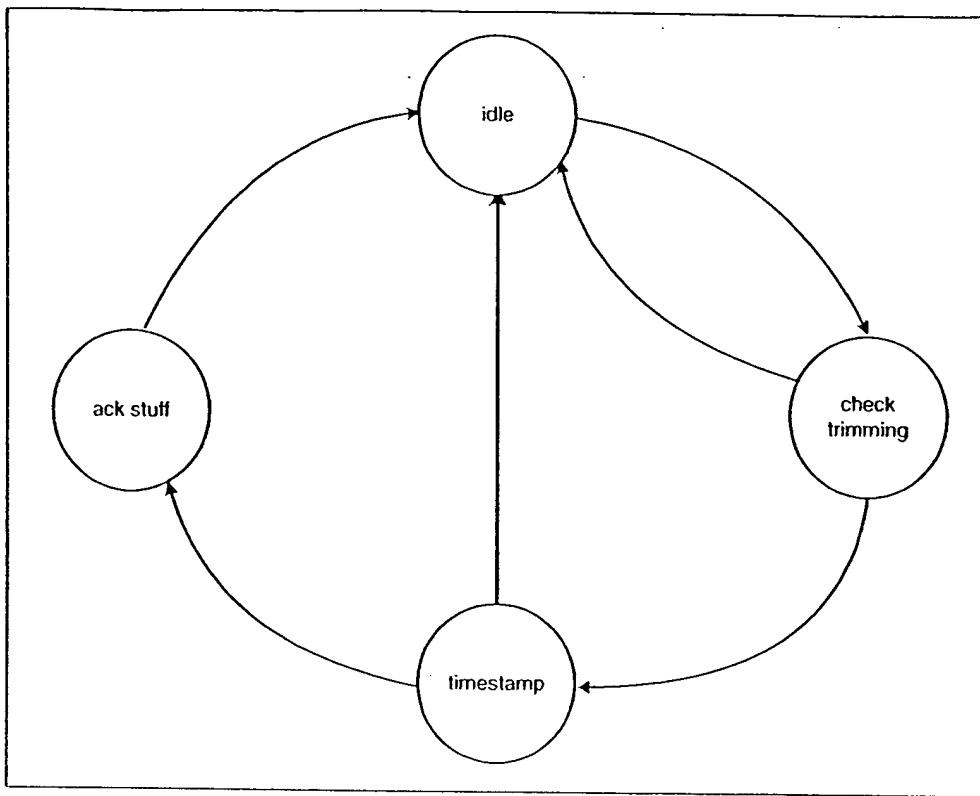


Figure 3C2: Validation State Machine

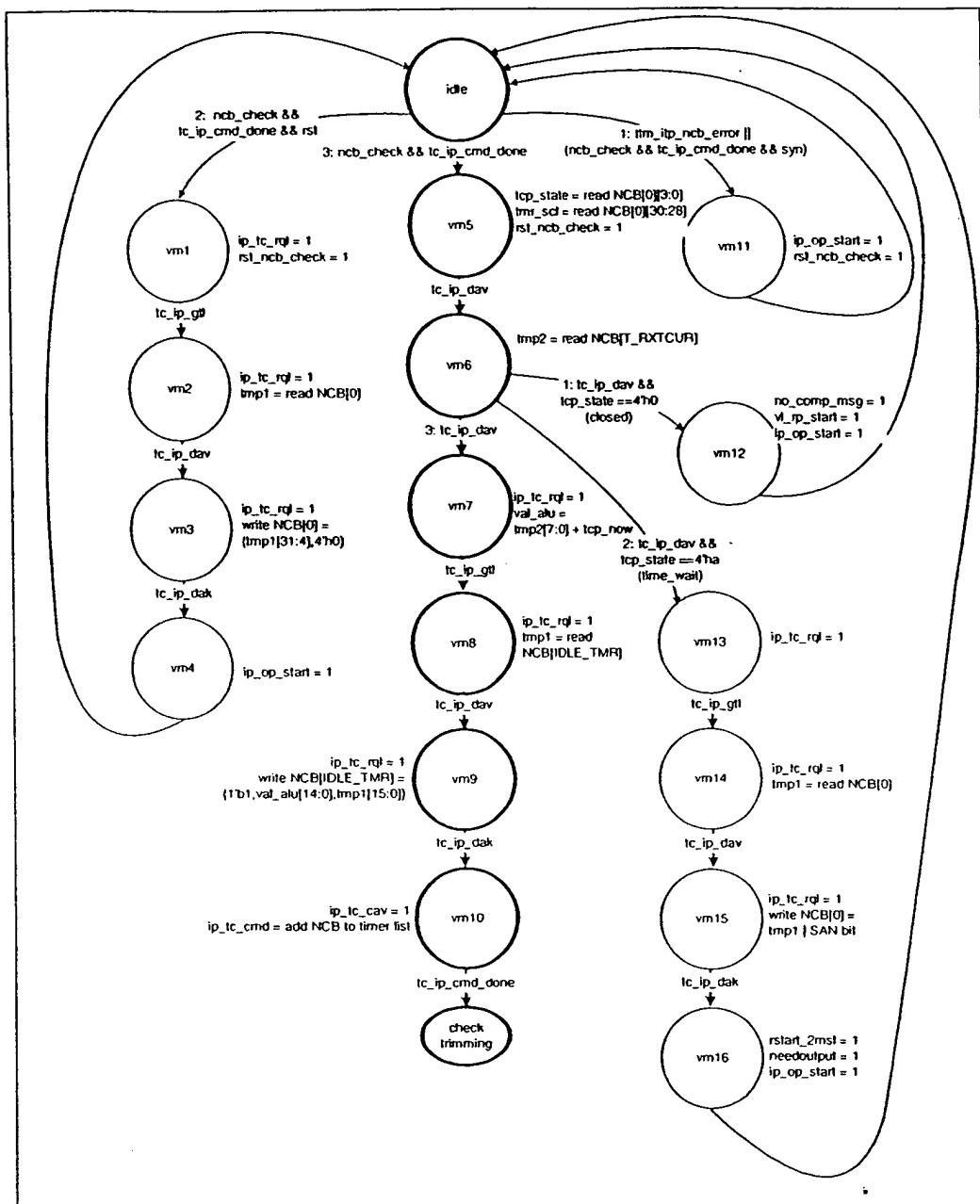


Figure 3C3: Validation State Machine for Reset, SYN or Invalid State

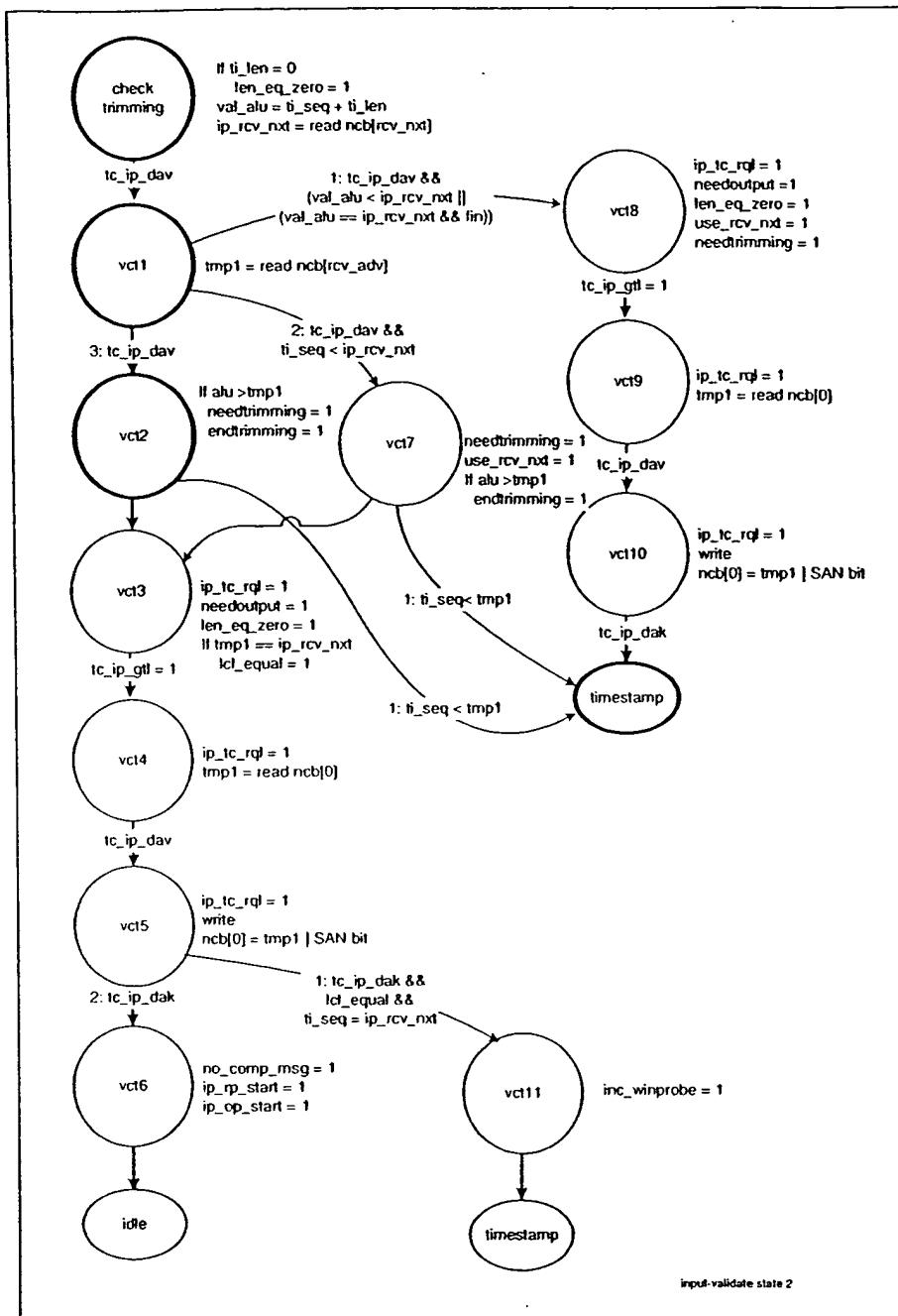


Figure 3C4: Validation State Machine for Trimming

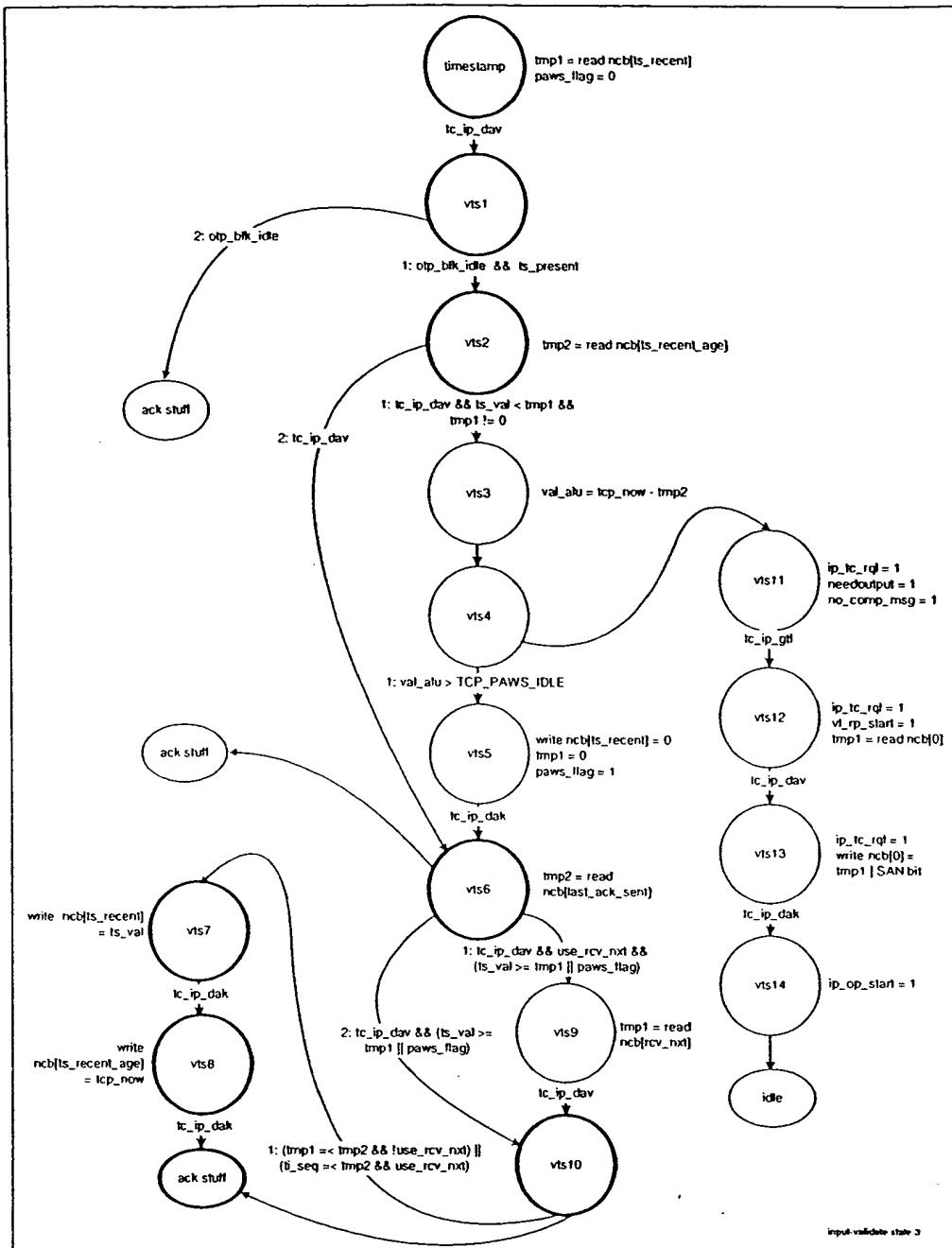


Figure 3C5: Validation State machine for Timestamp functionality

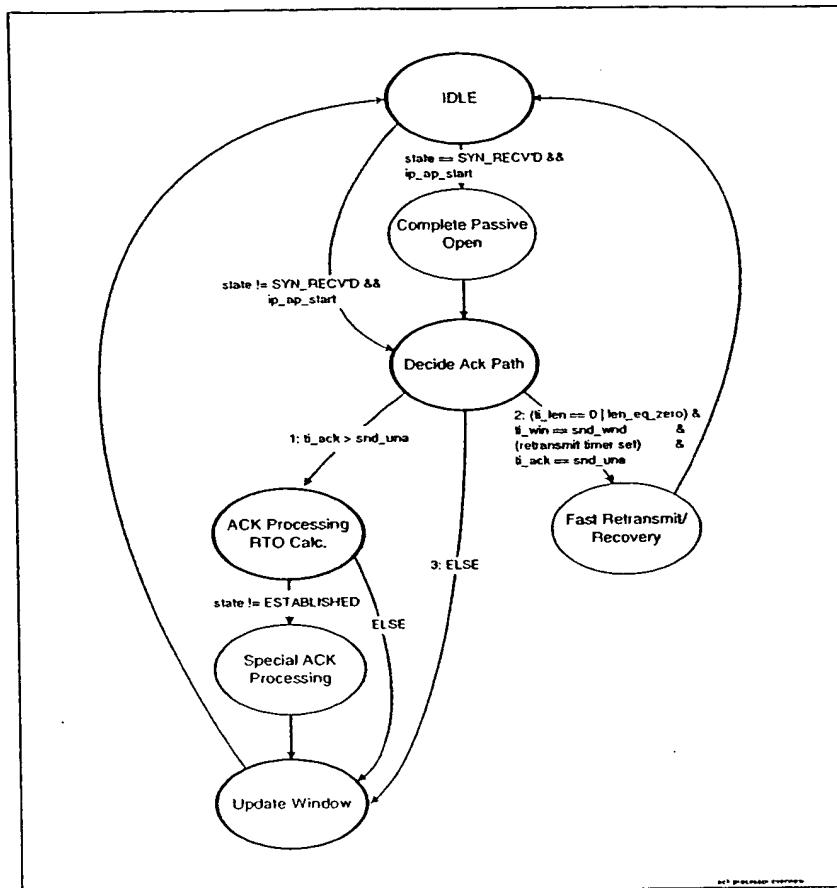


Figure 3C6—ACK Processor

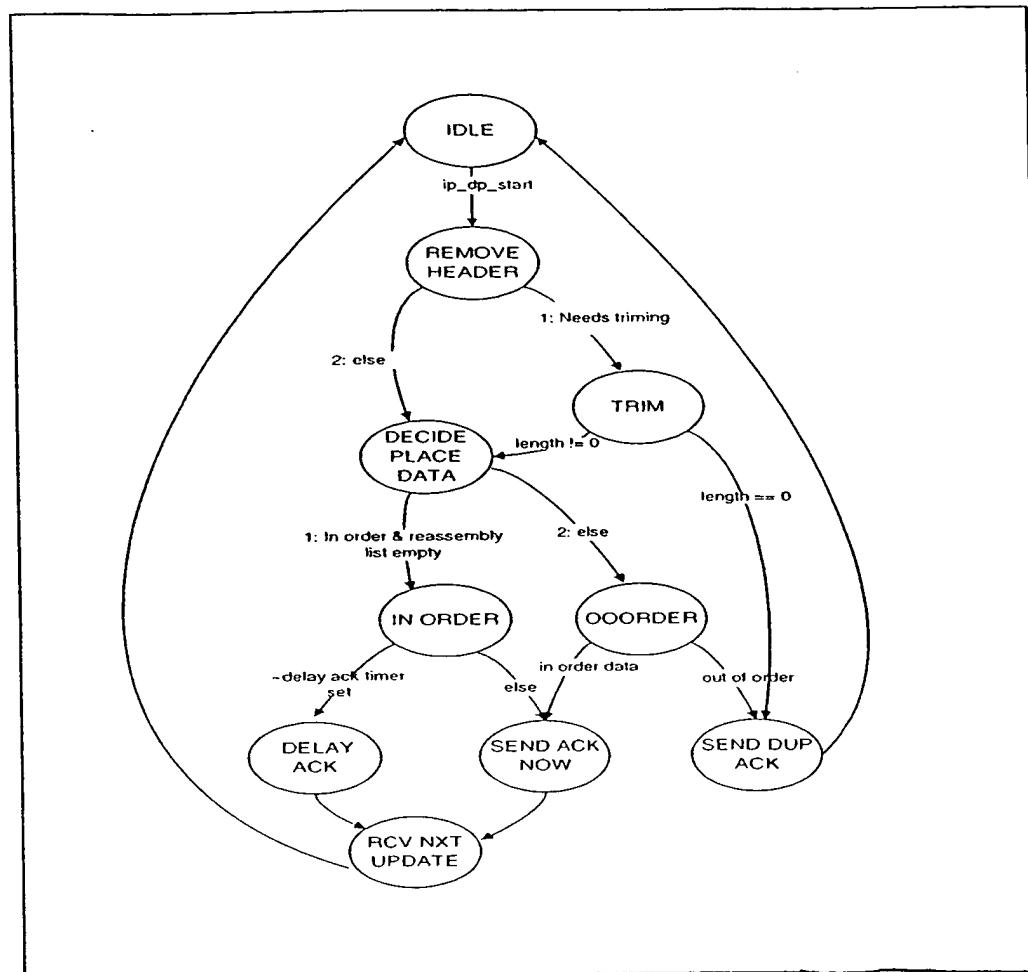


Figure 3C7 Data Processing State Machine

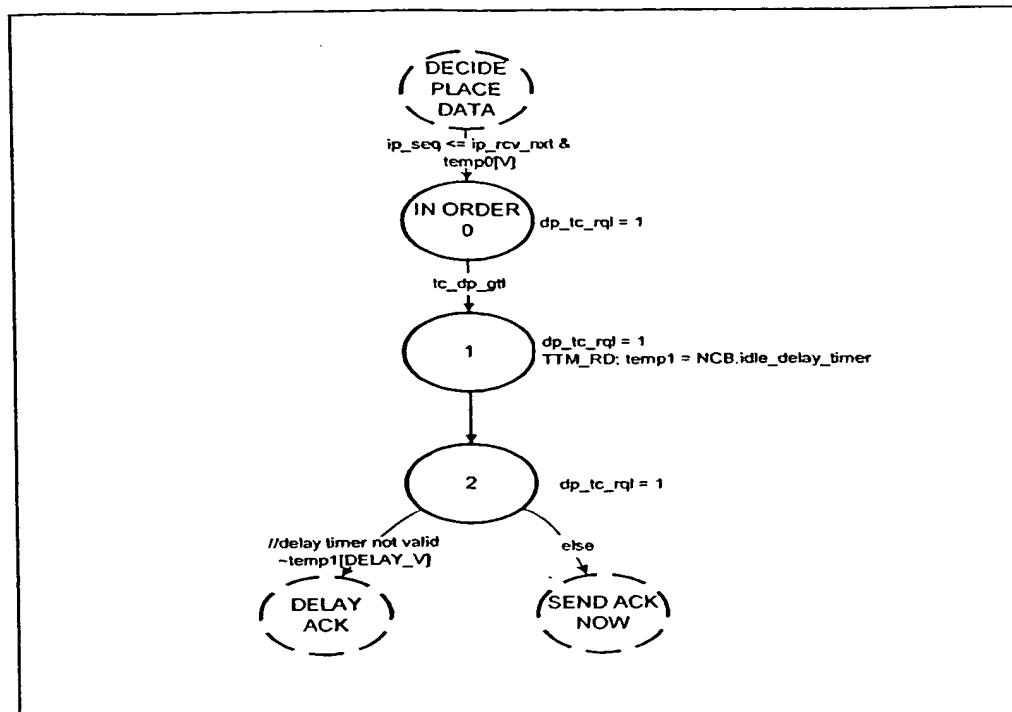


Figure 3C8 – Data Process Inorder

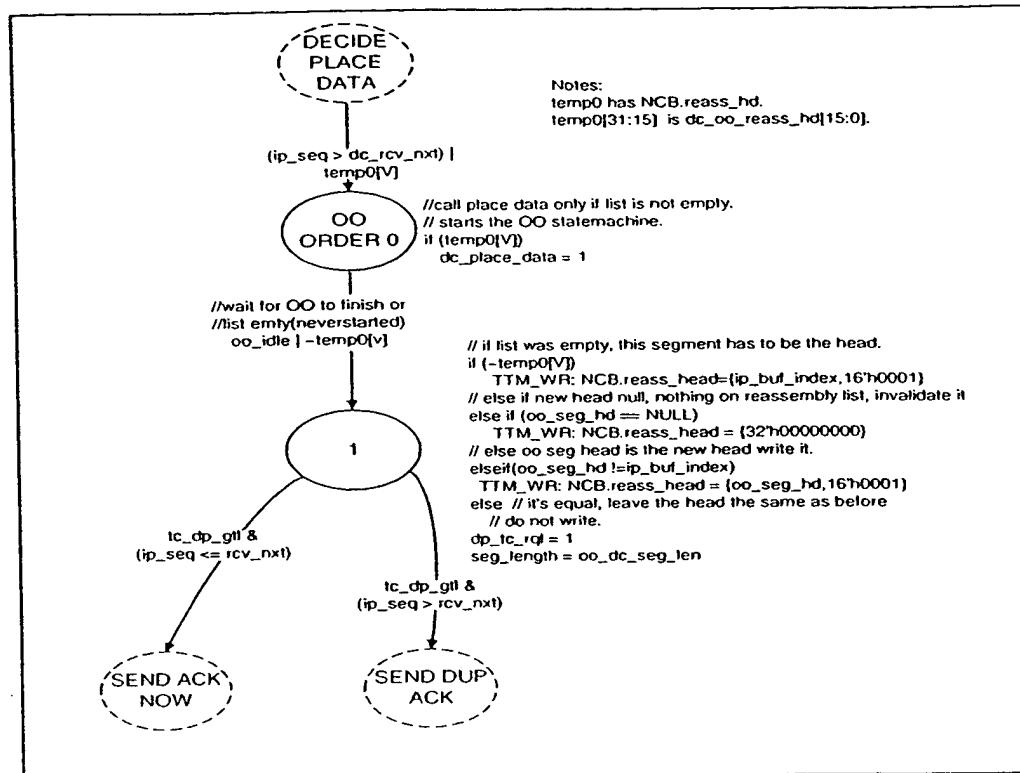


Figure 3C9 – Data Process Out of Order

220651

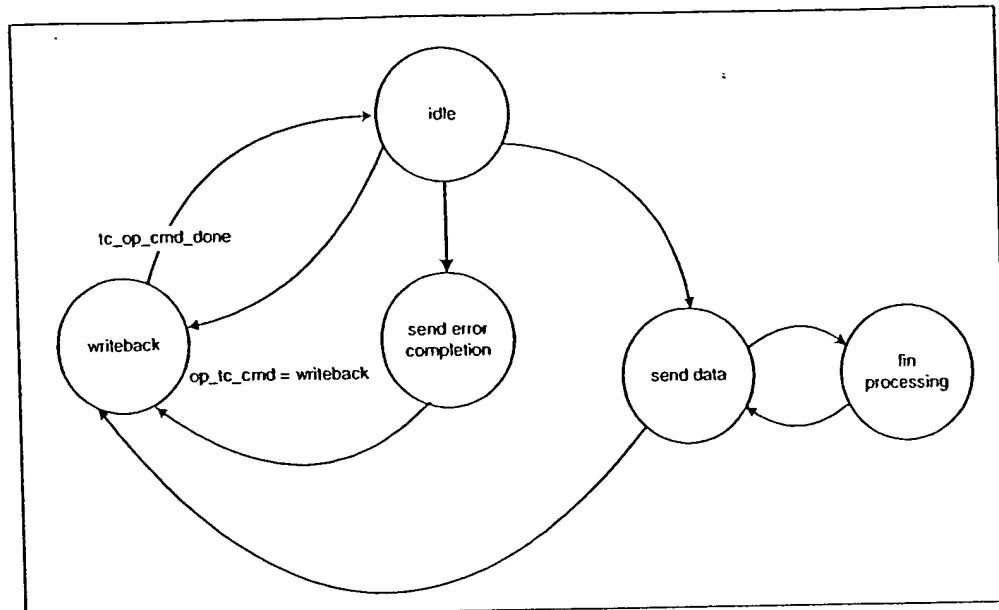


Figure 3C10 - ITP Output Processor

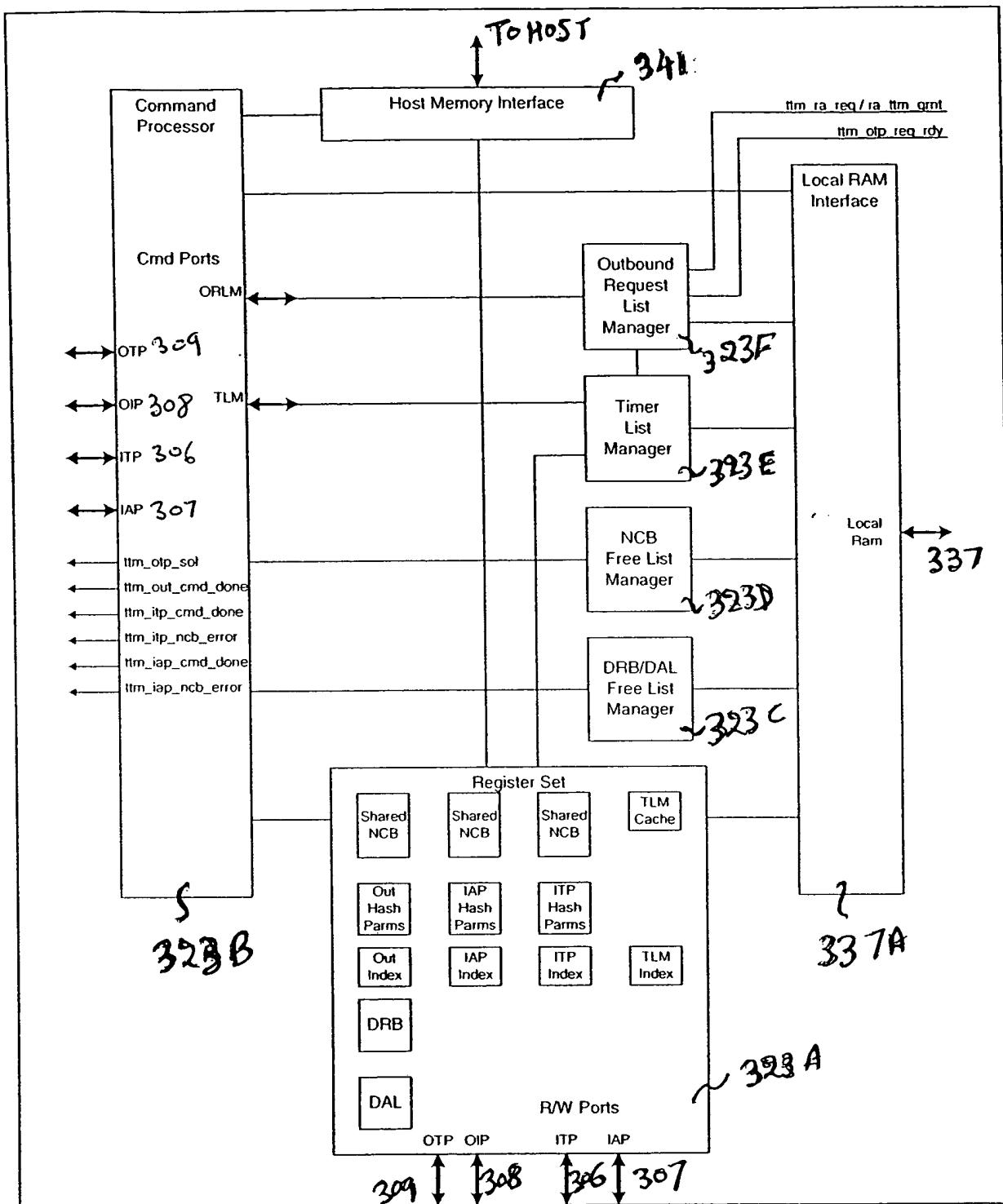


Figure 3D?

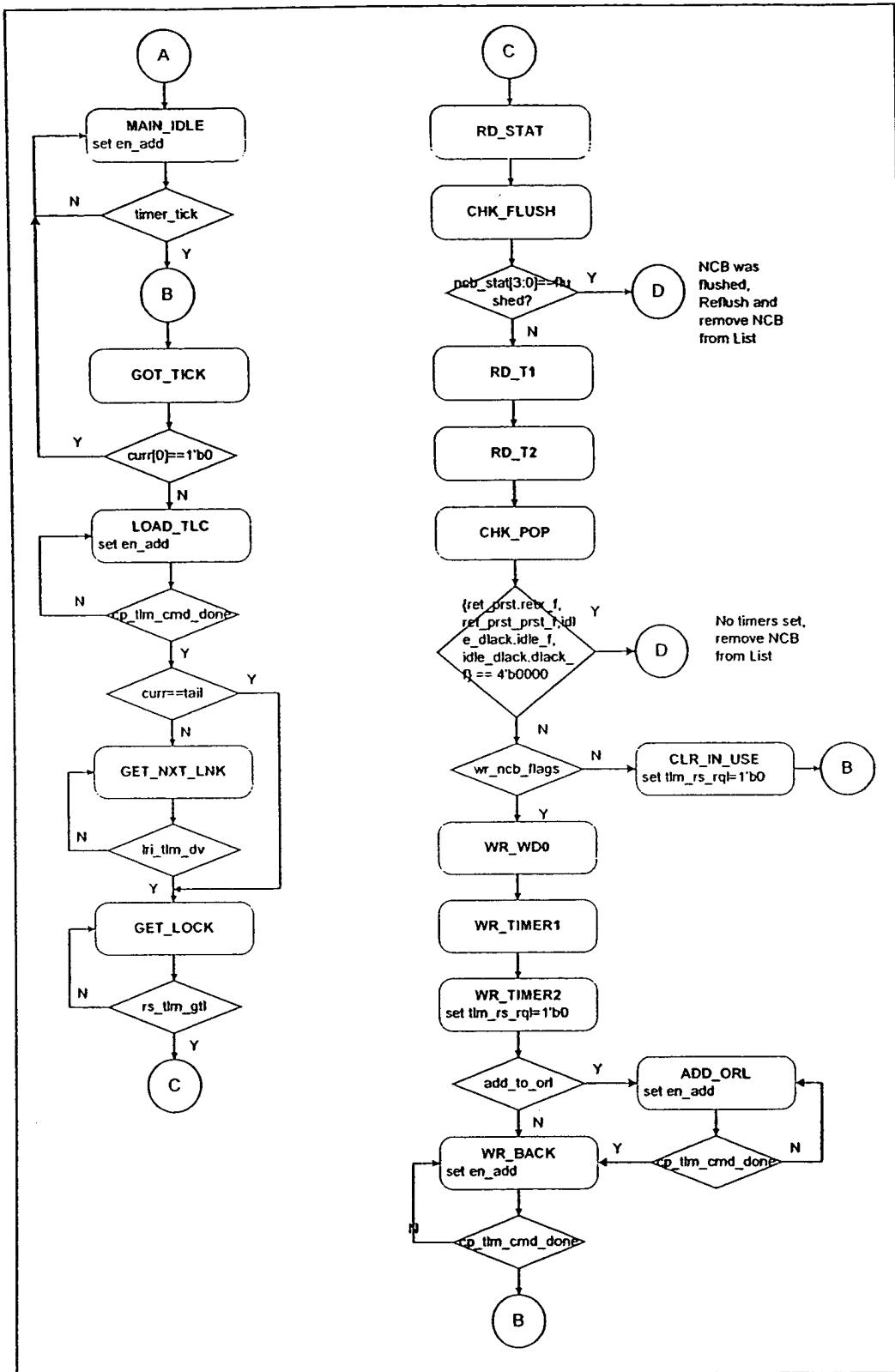


Figure 3DI Timer List Manager State Machine

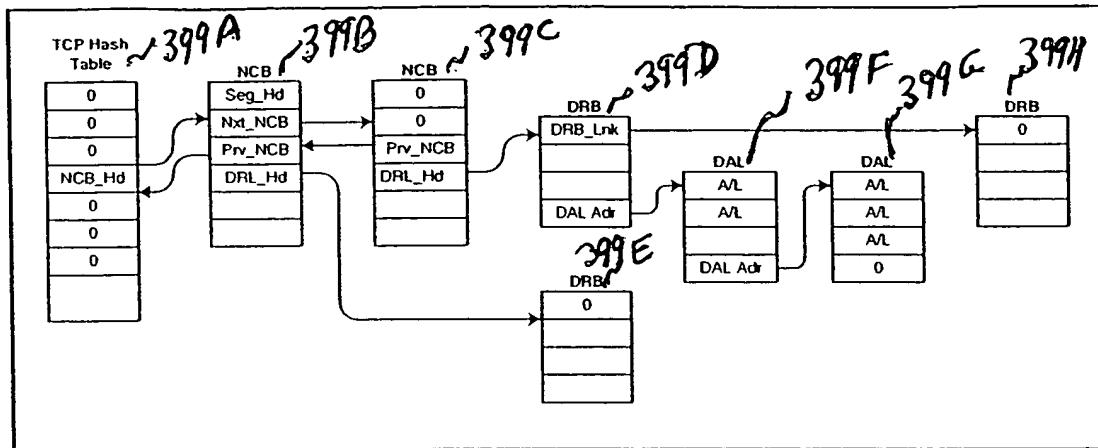


Figure 3E

399

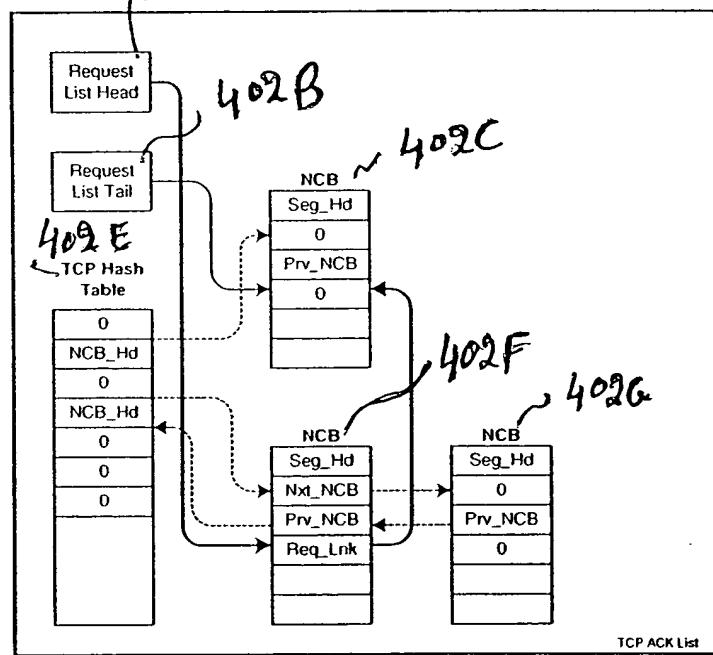


Figure 3F

402

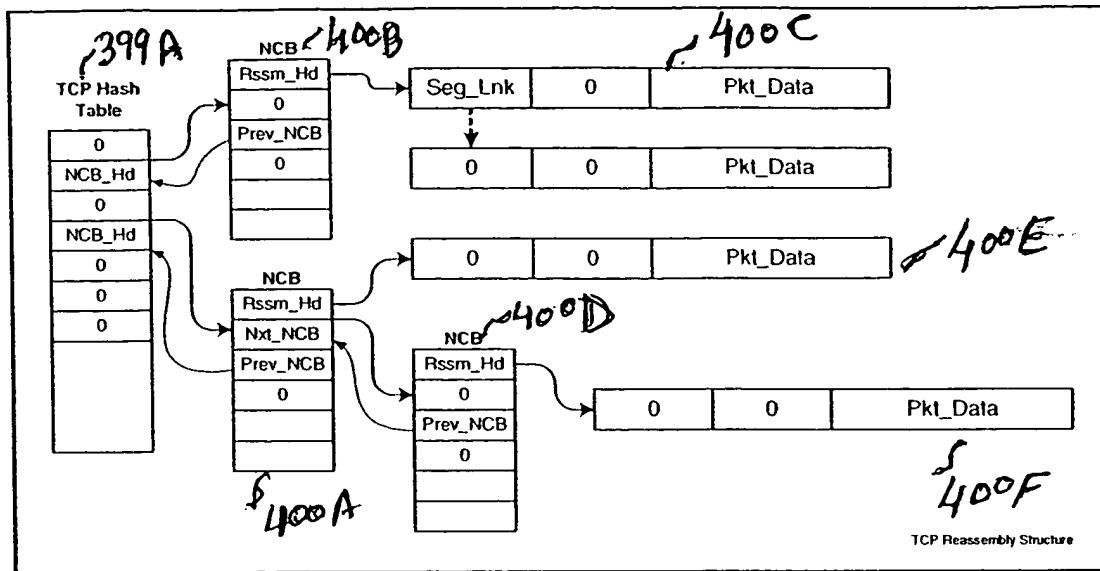


Figure 3G

400  
↑

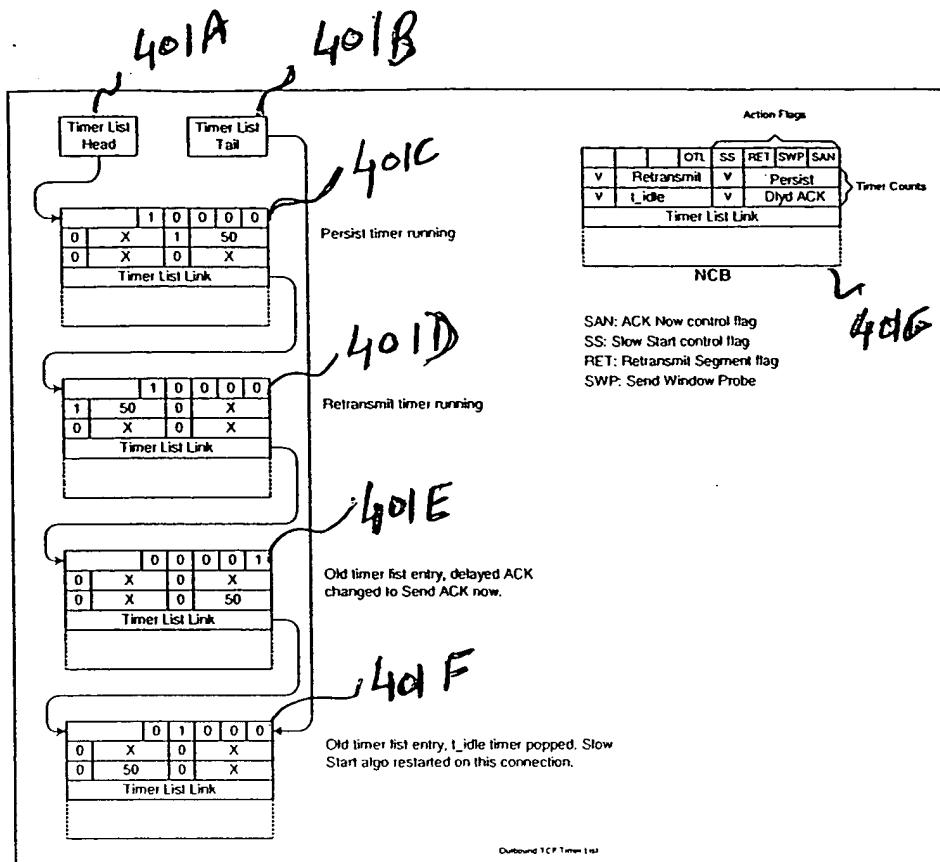


Figure 3H

401

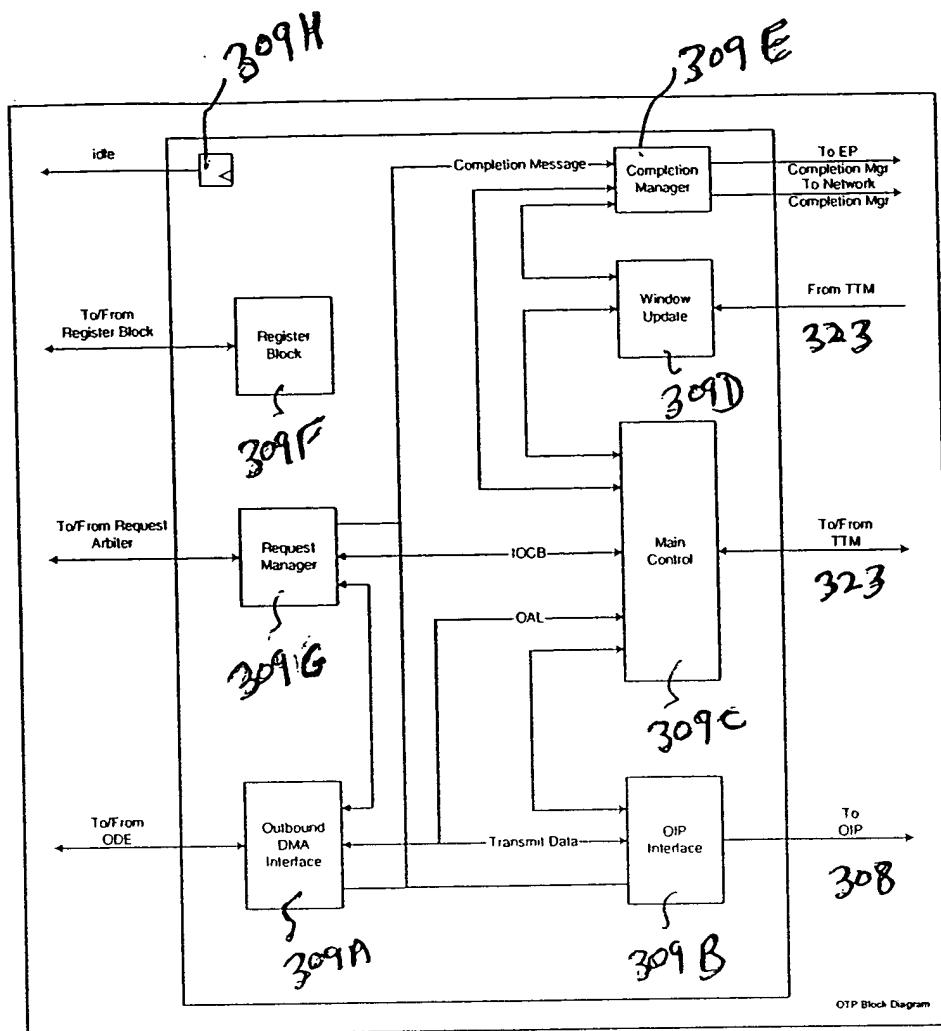


Figure 31

309

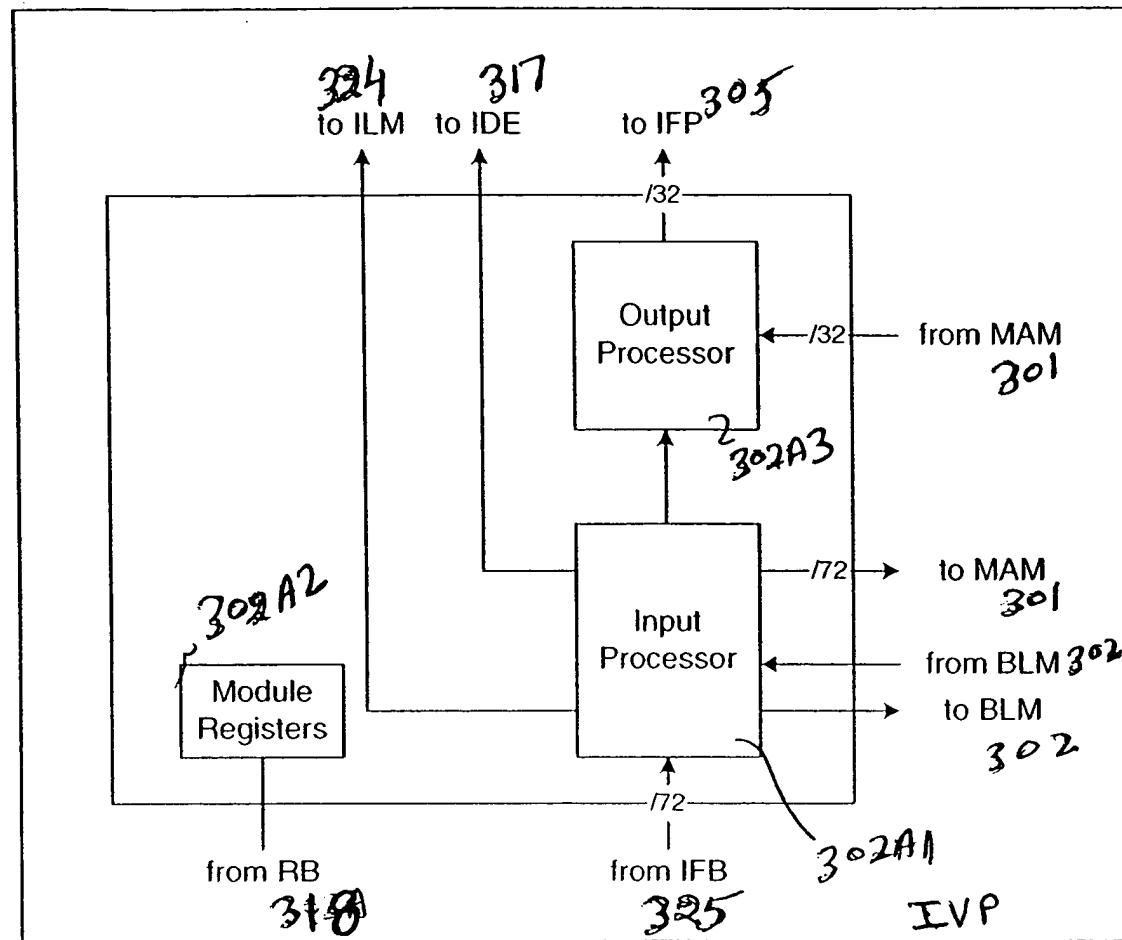


Figure 3J1

302A

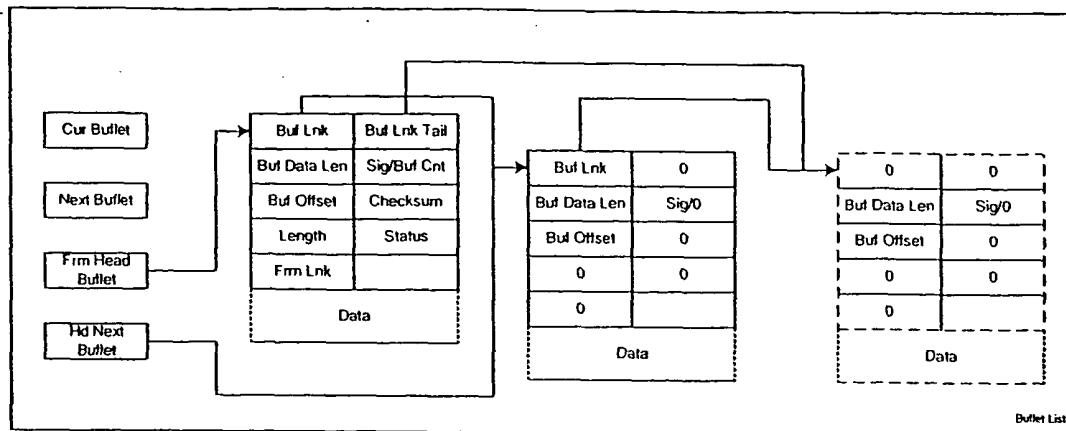


Figure 3J1 Buflet List Structure

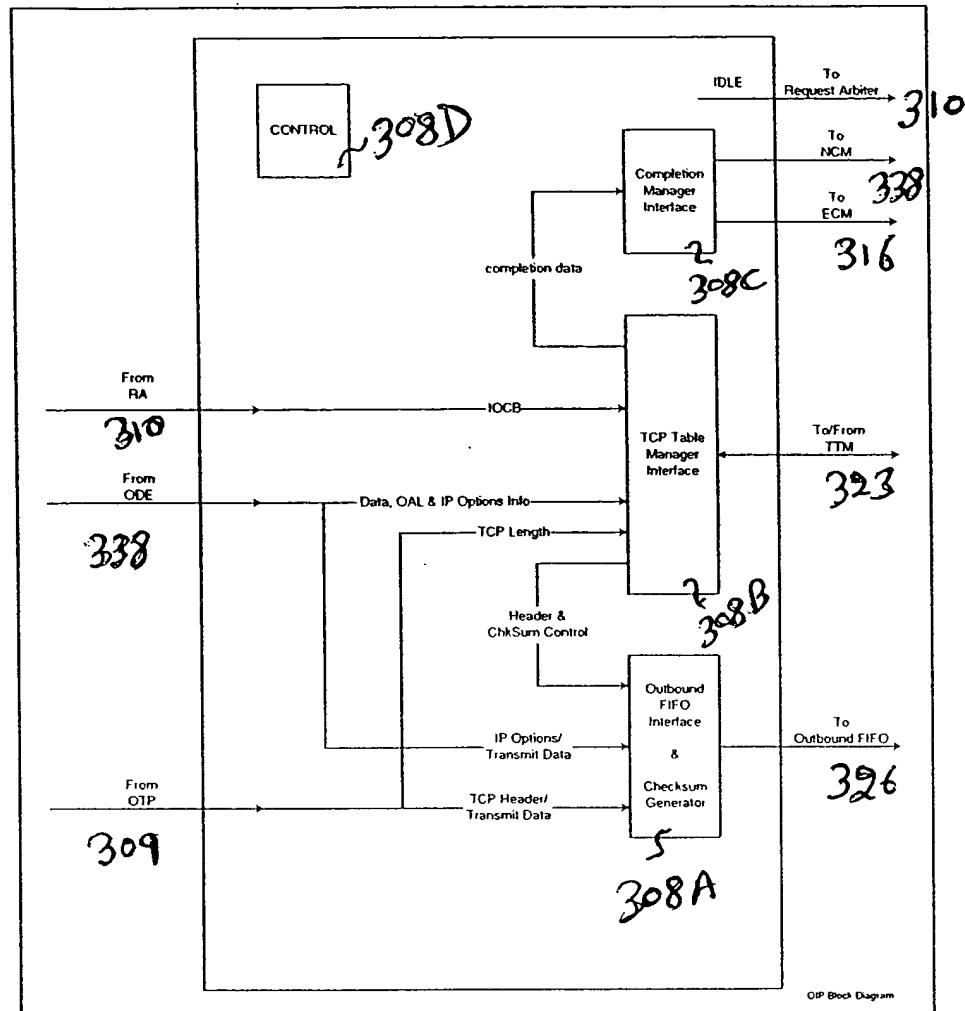


Figure 3K

308

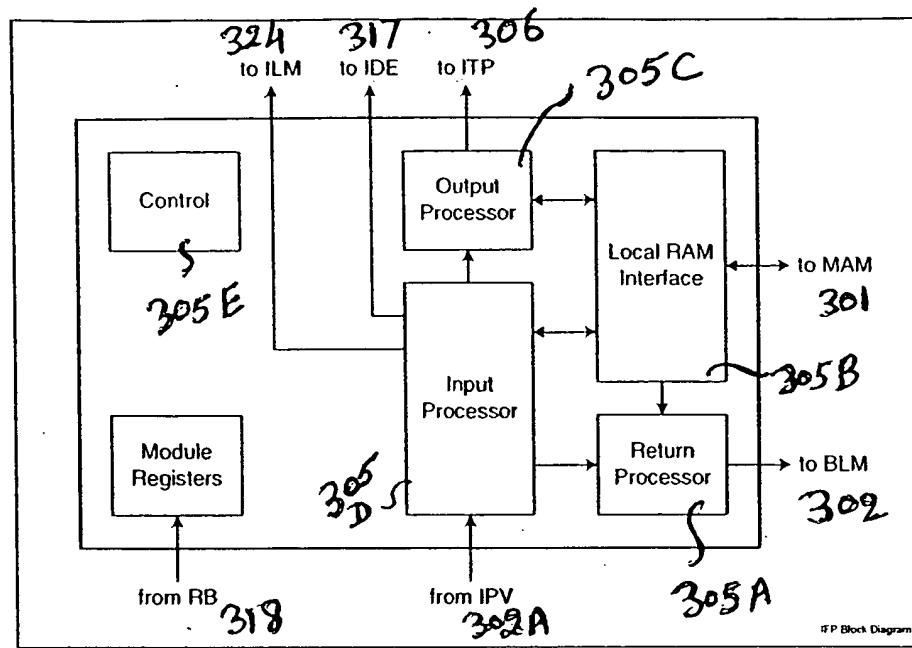


Figure 3L

305

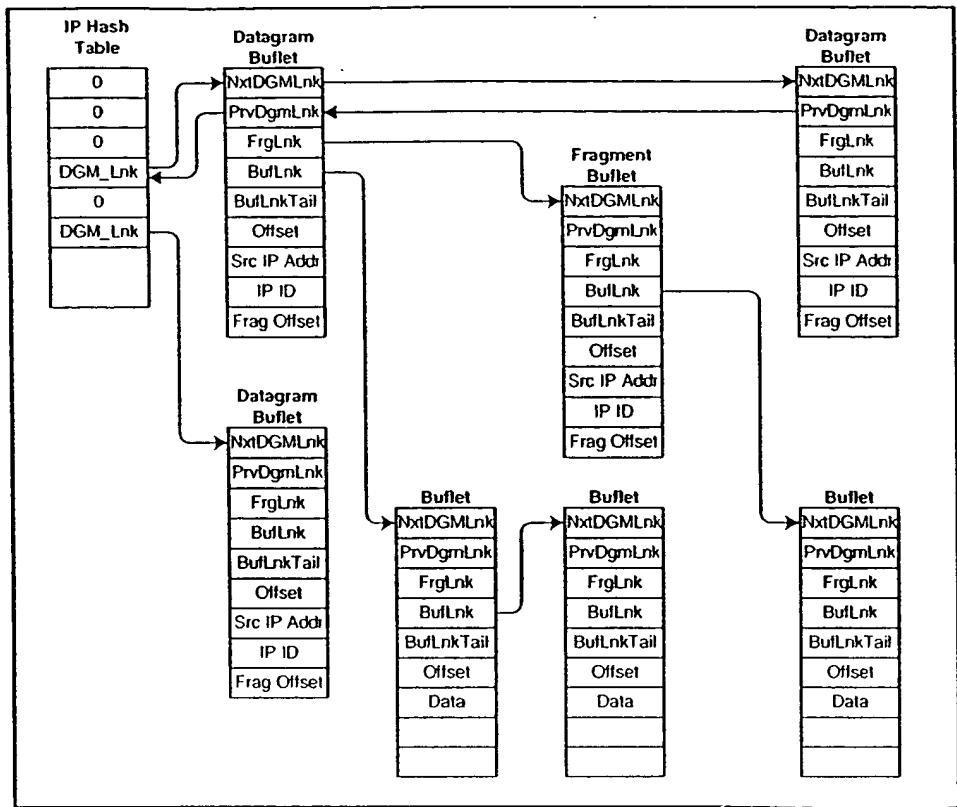


Figure 3L1:

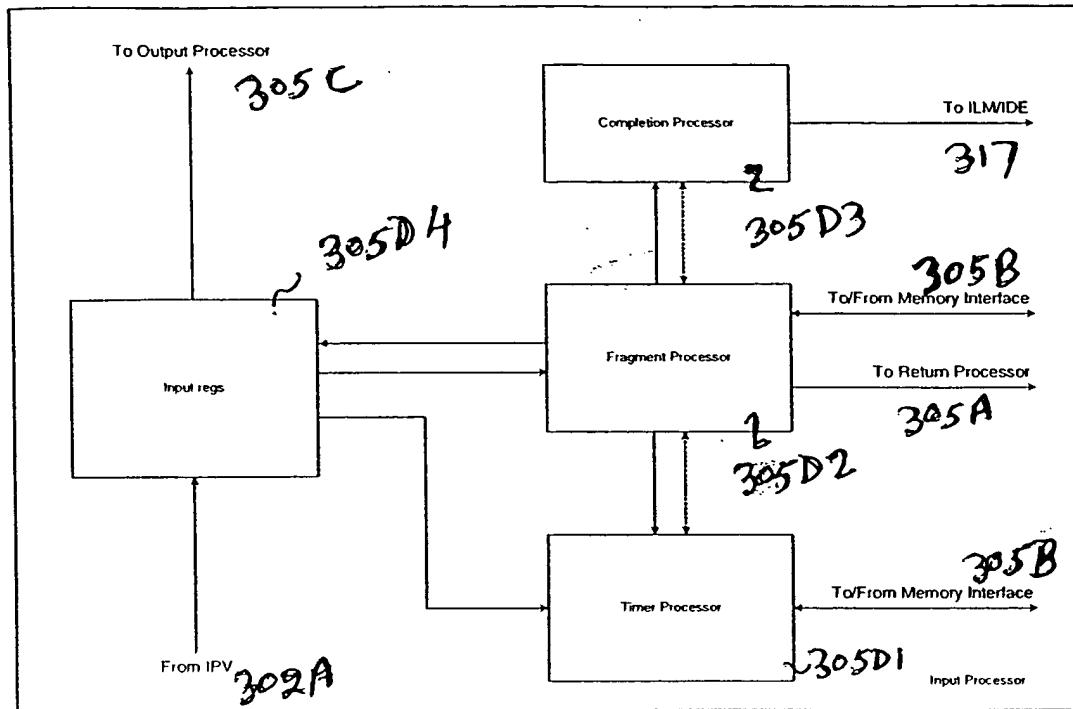


Figure 3L2

305D

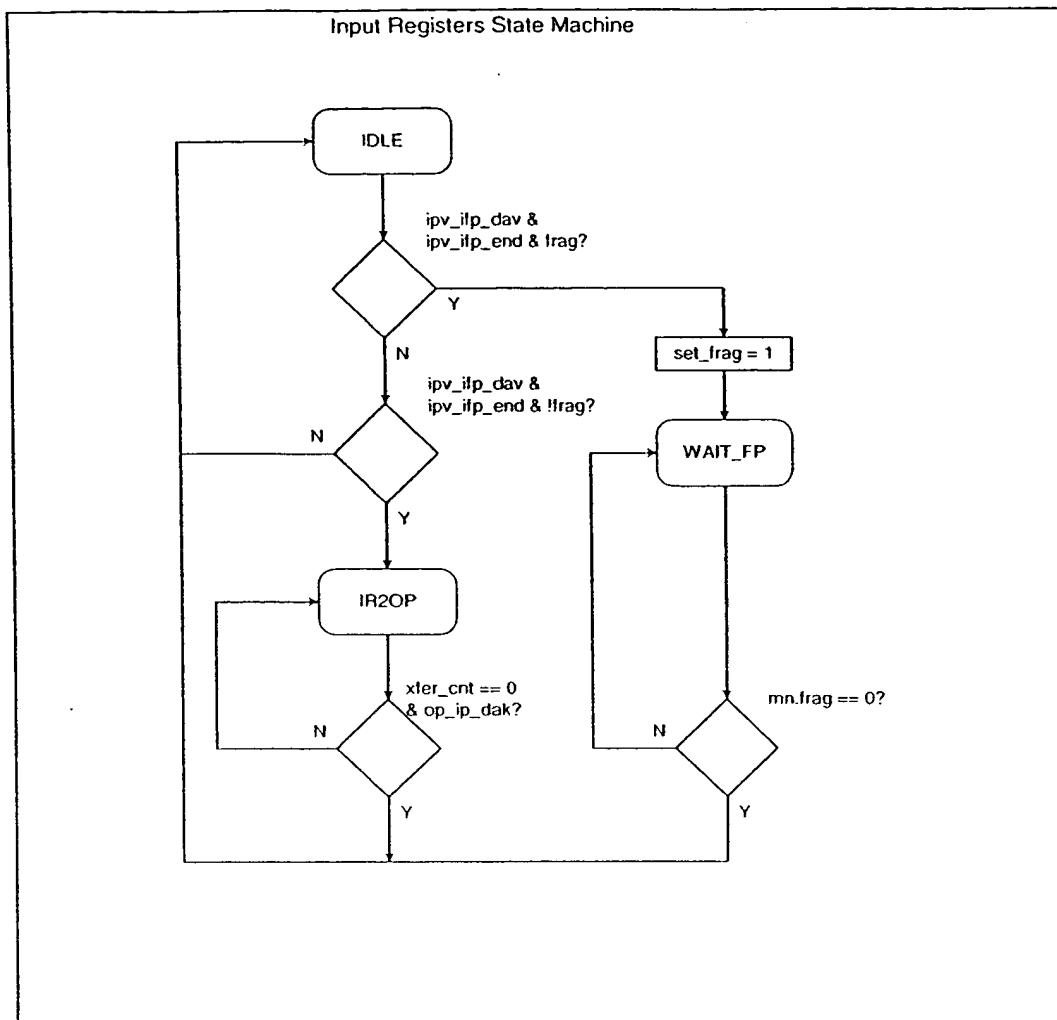


Figure 3L3

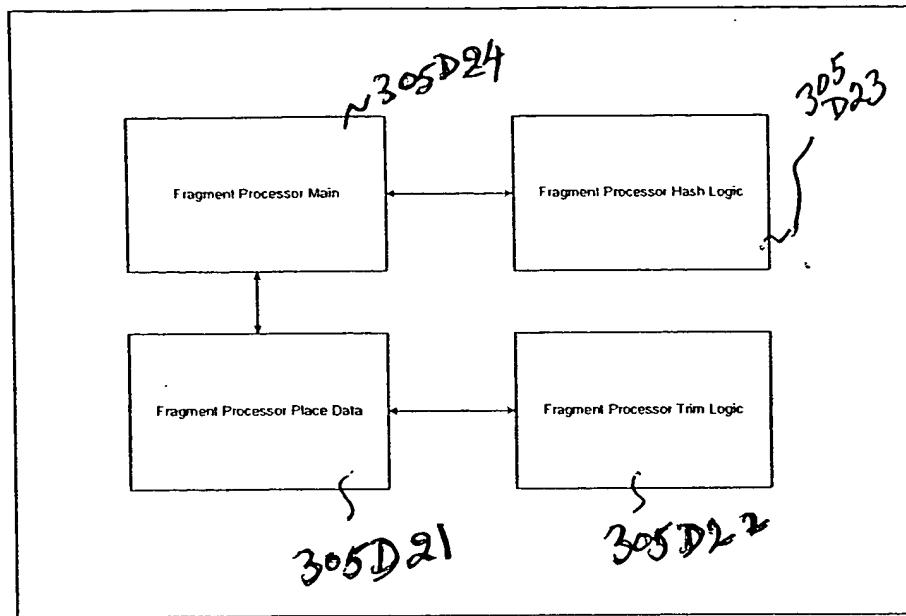


Figure 3L4

305D2

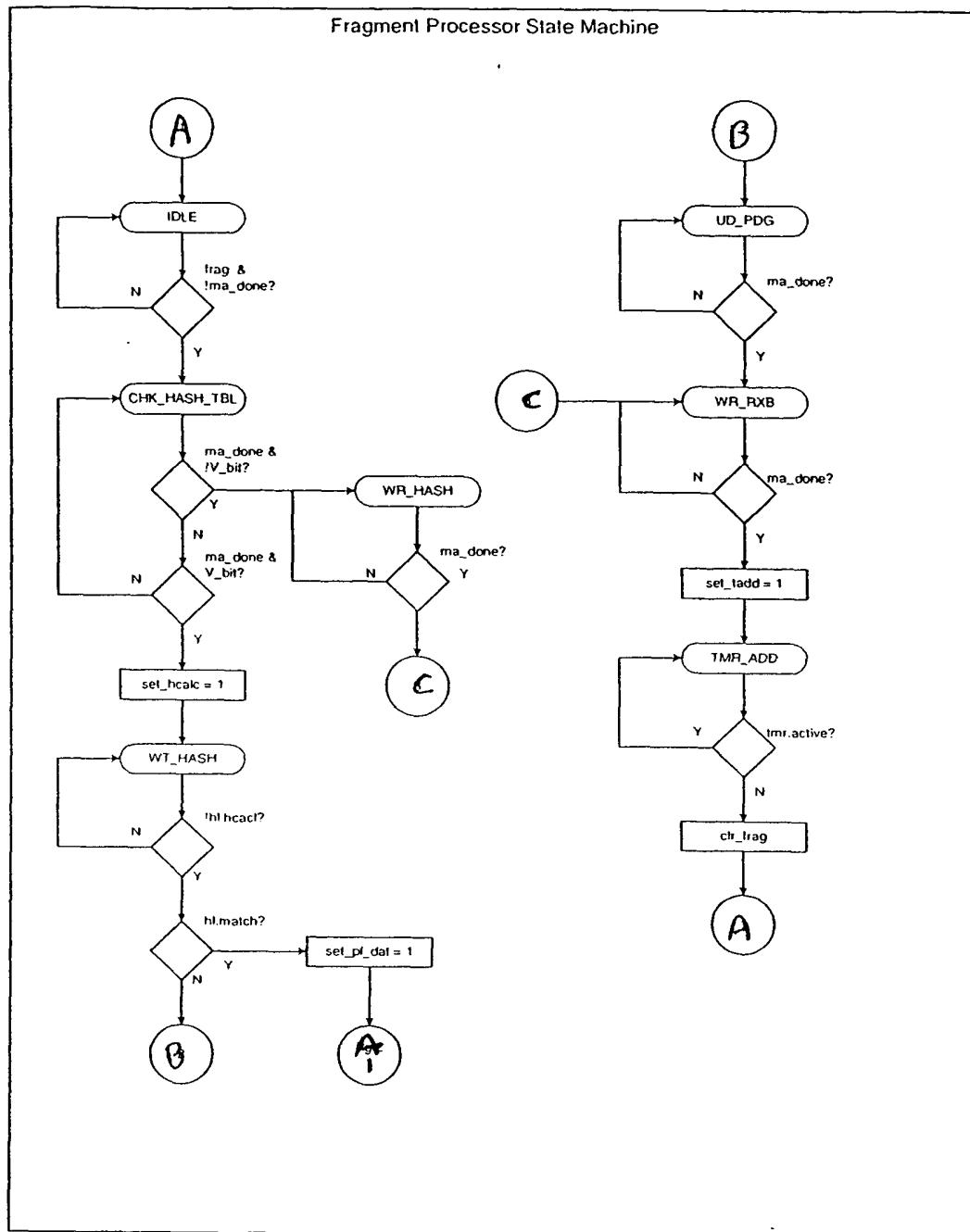


Figure 3L5A I

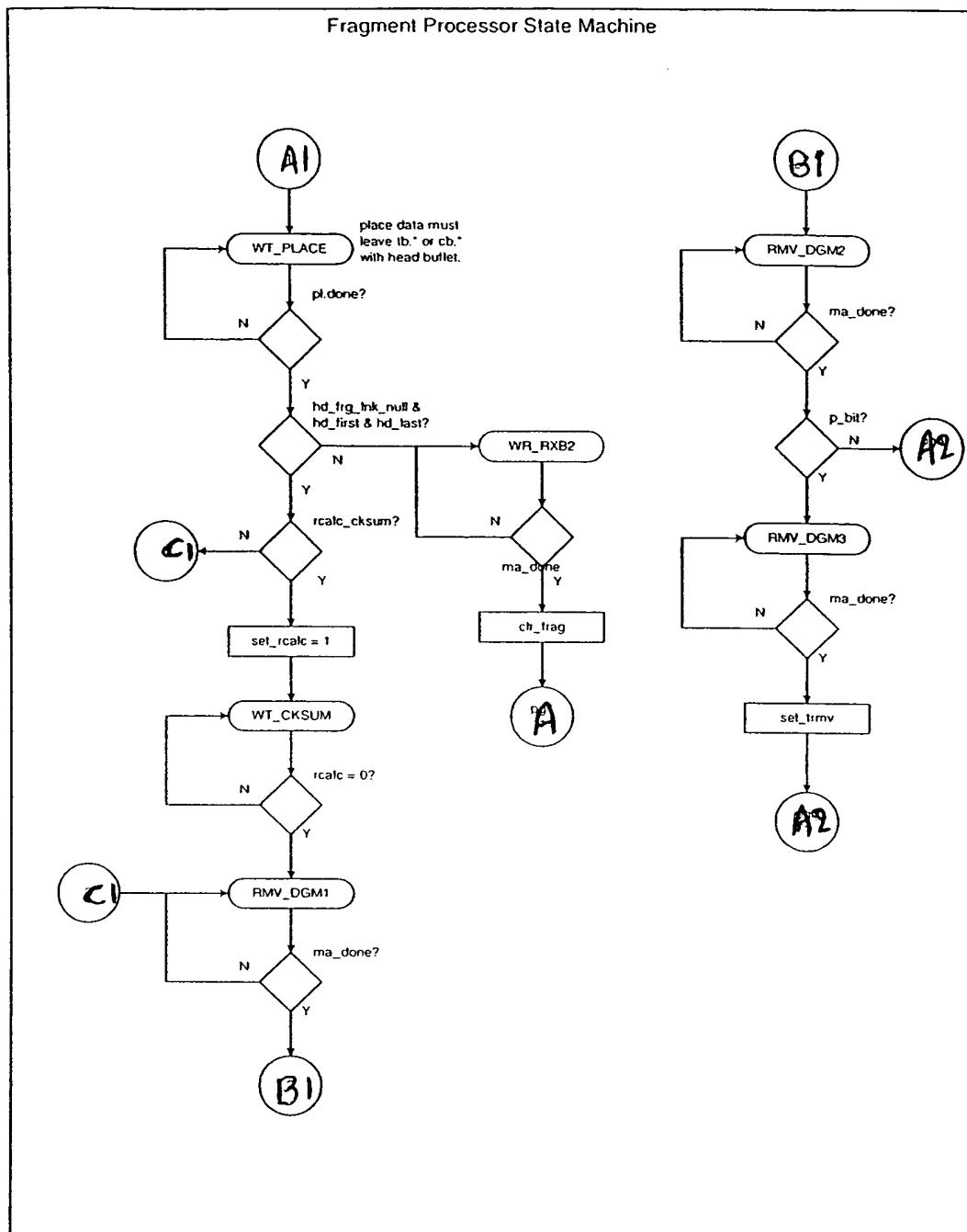


Figure 3L5B\_F

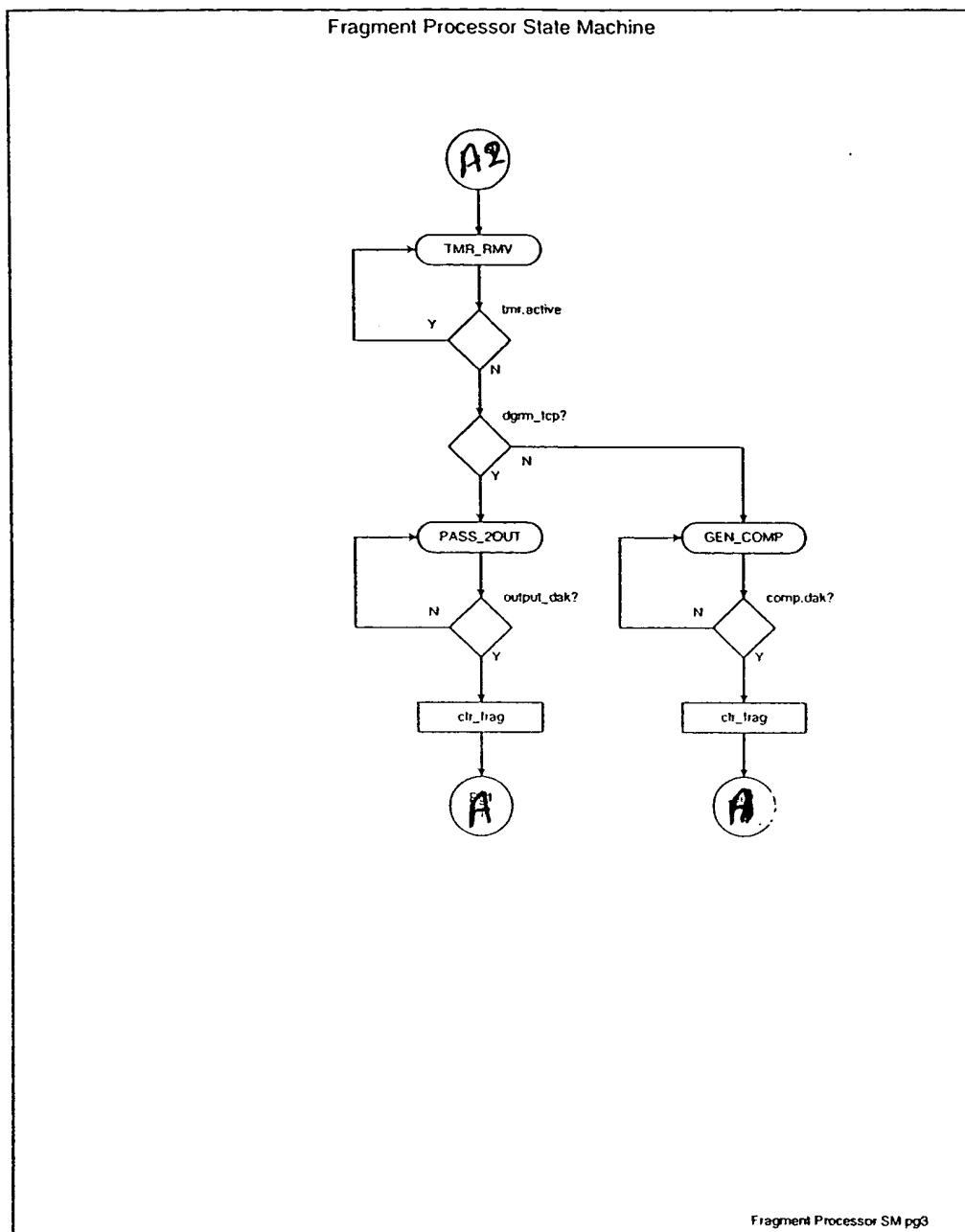


Figure 3L5C

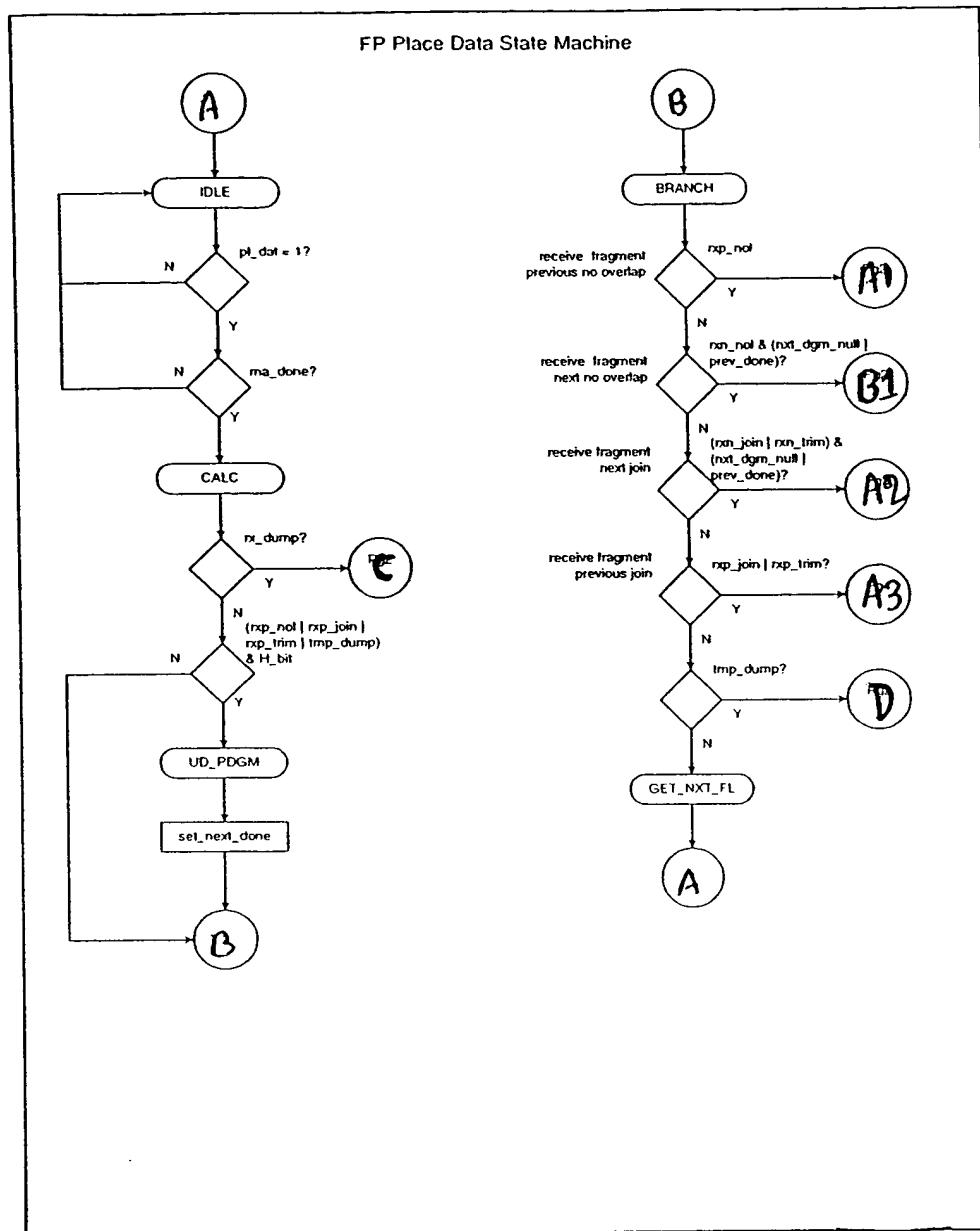


Figure 3L6A

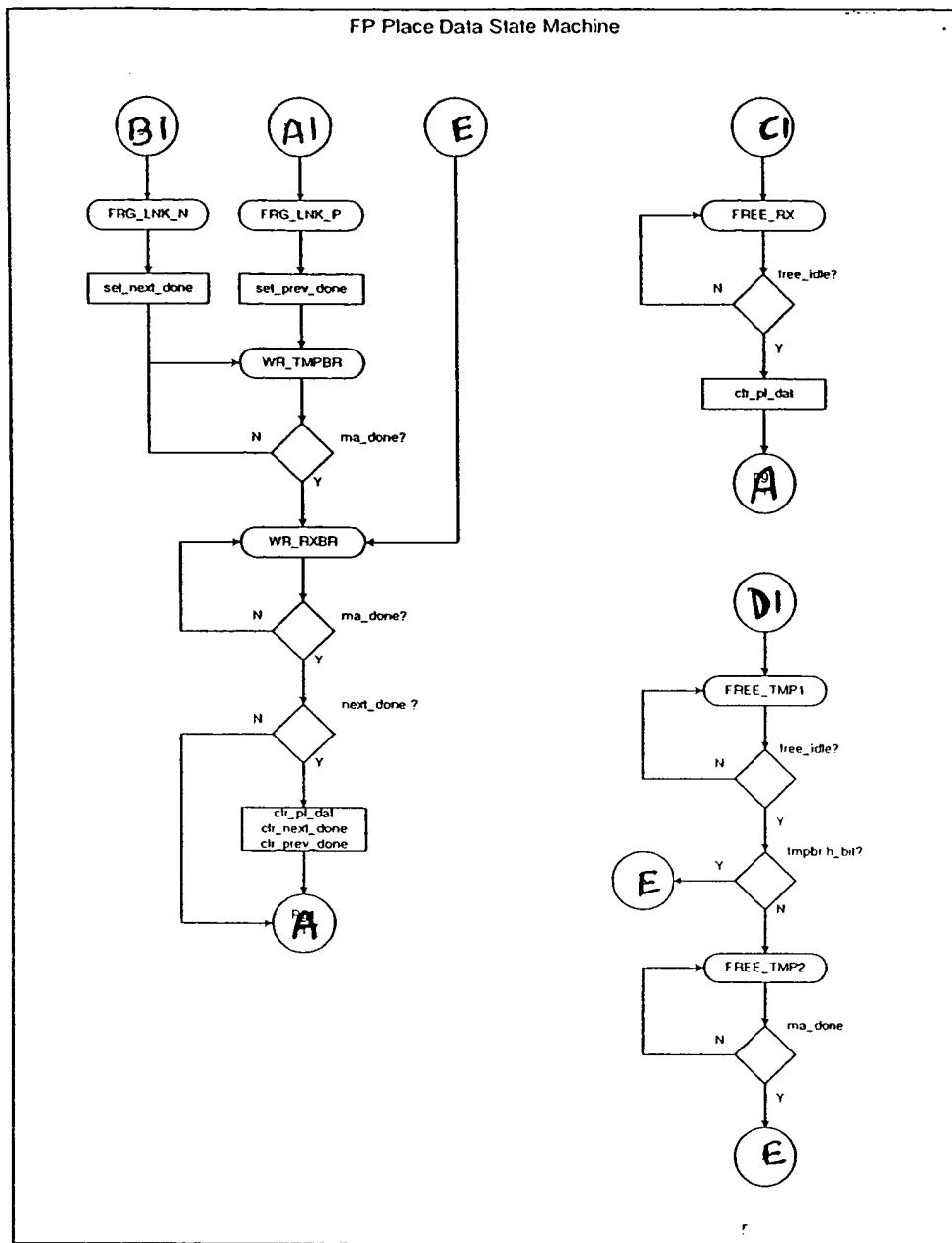


Figure 3L6 B

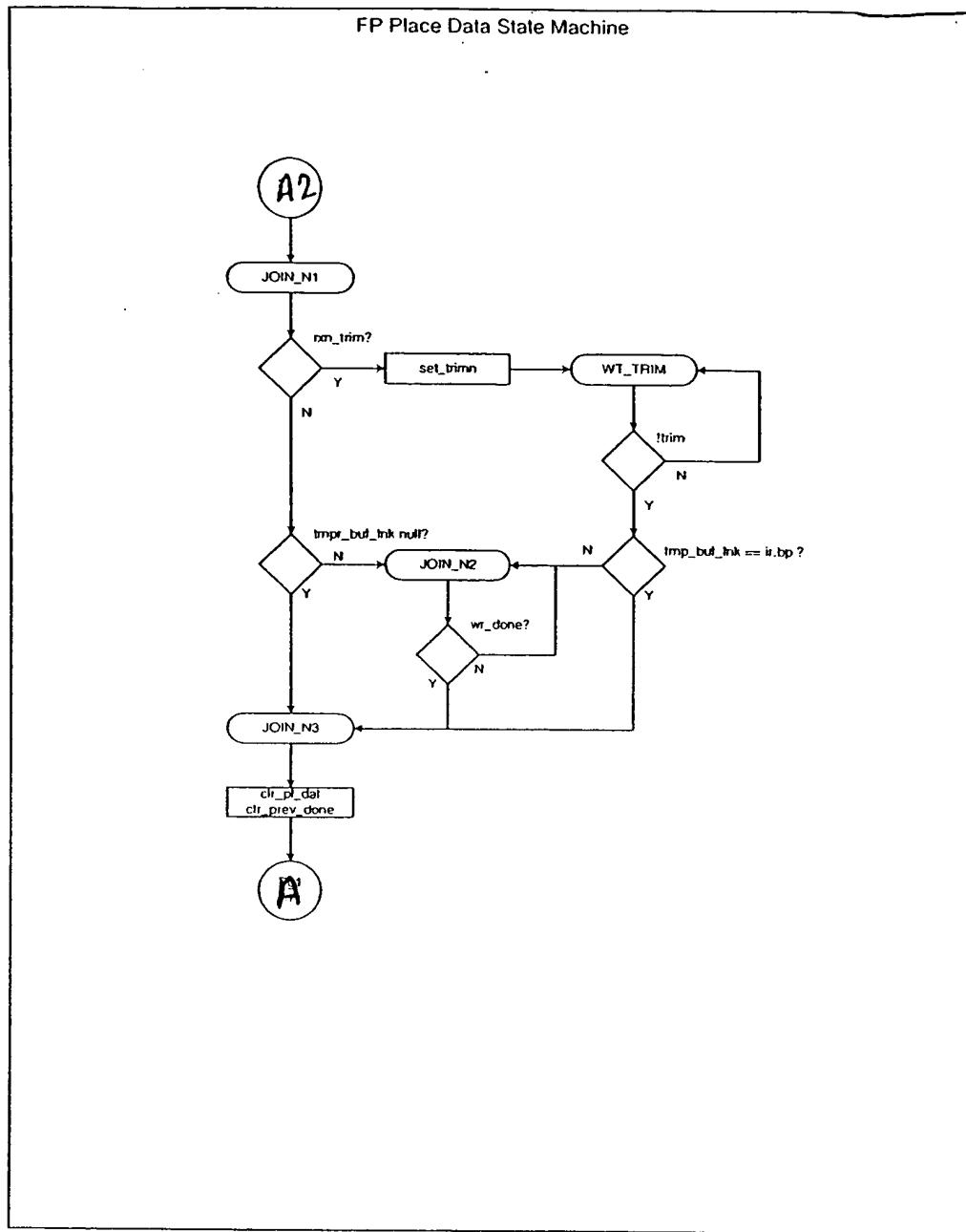


Figure 3L6C

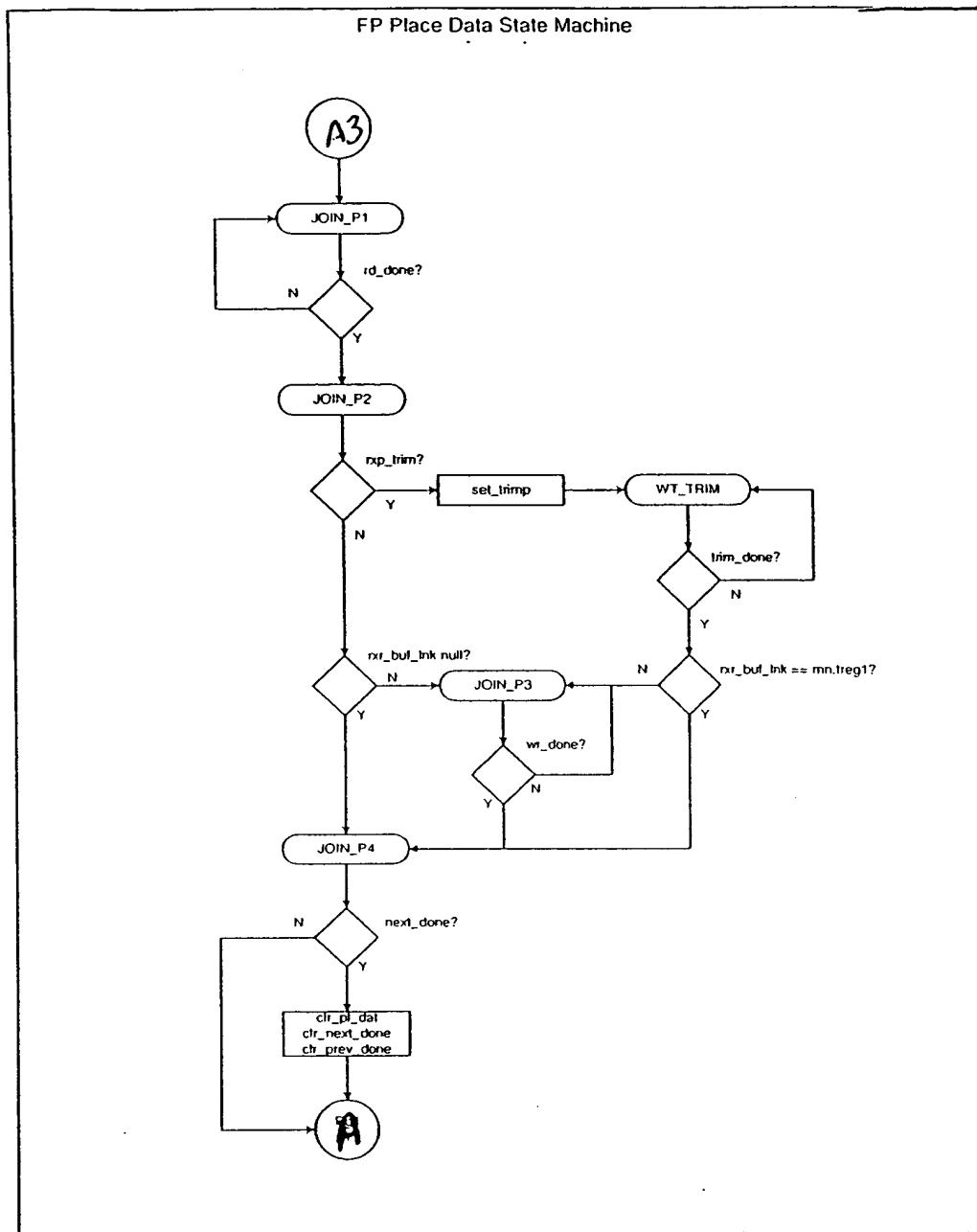


Figure 3L6D

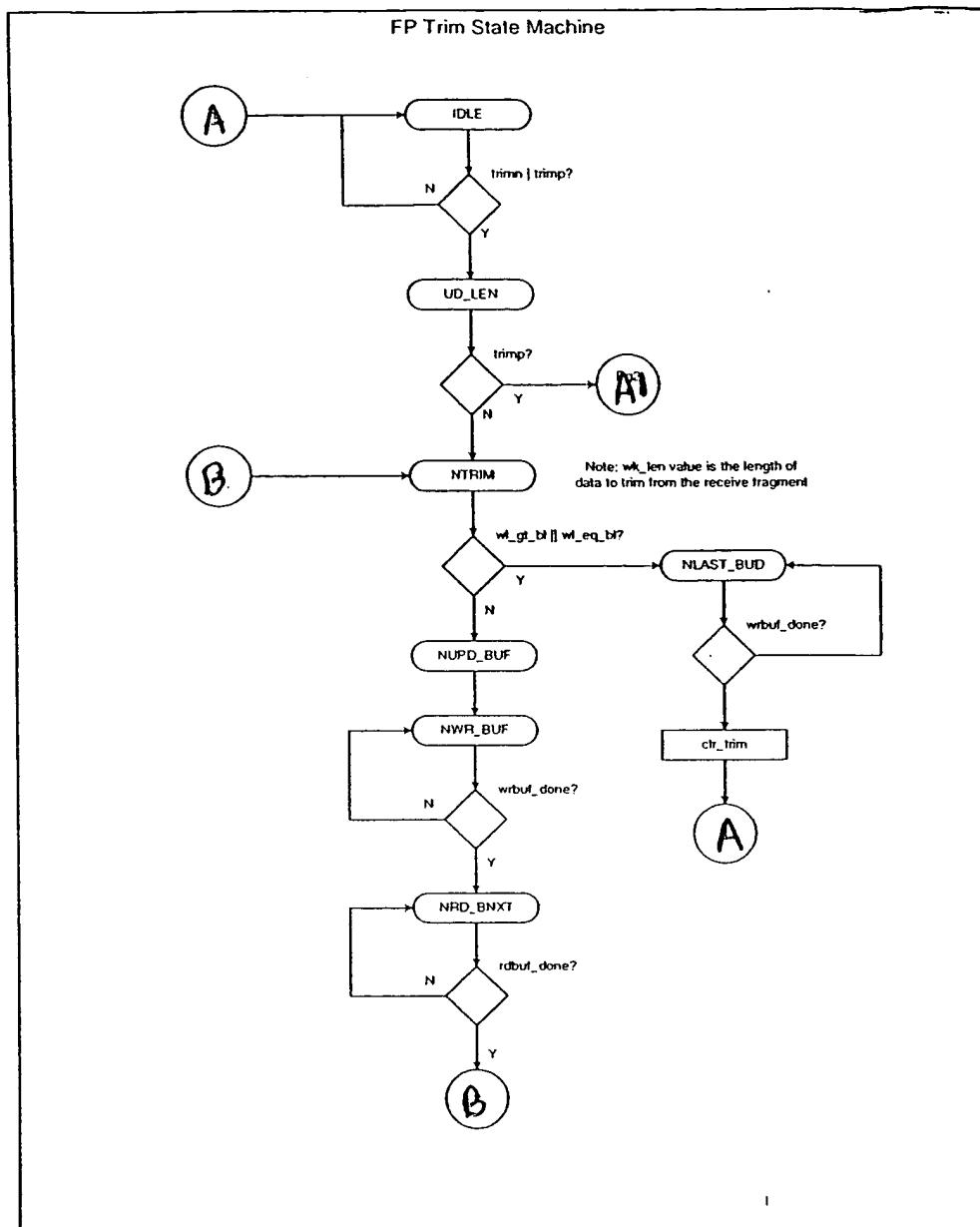


Figure 3L7A

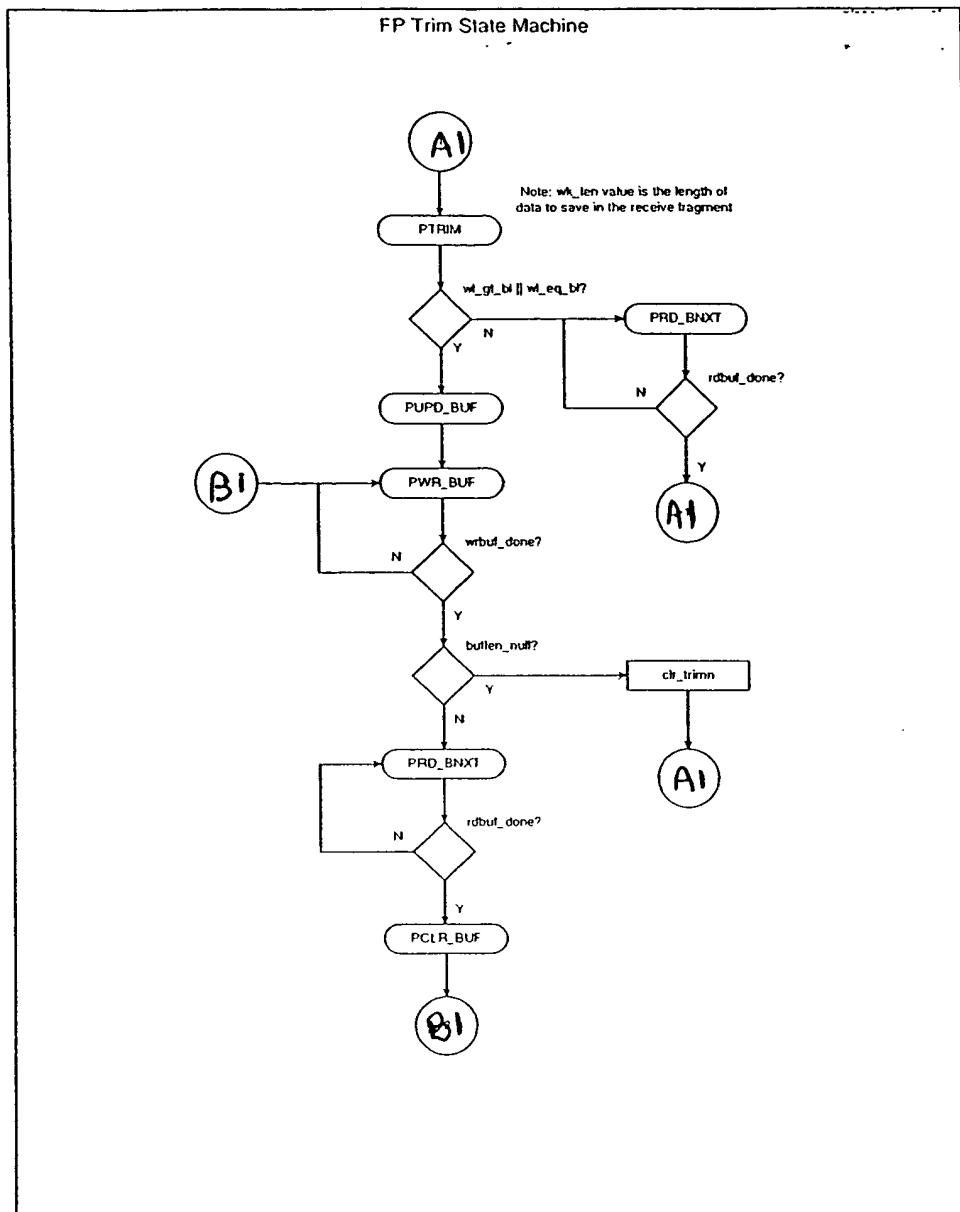


Figure 3L7B:

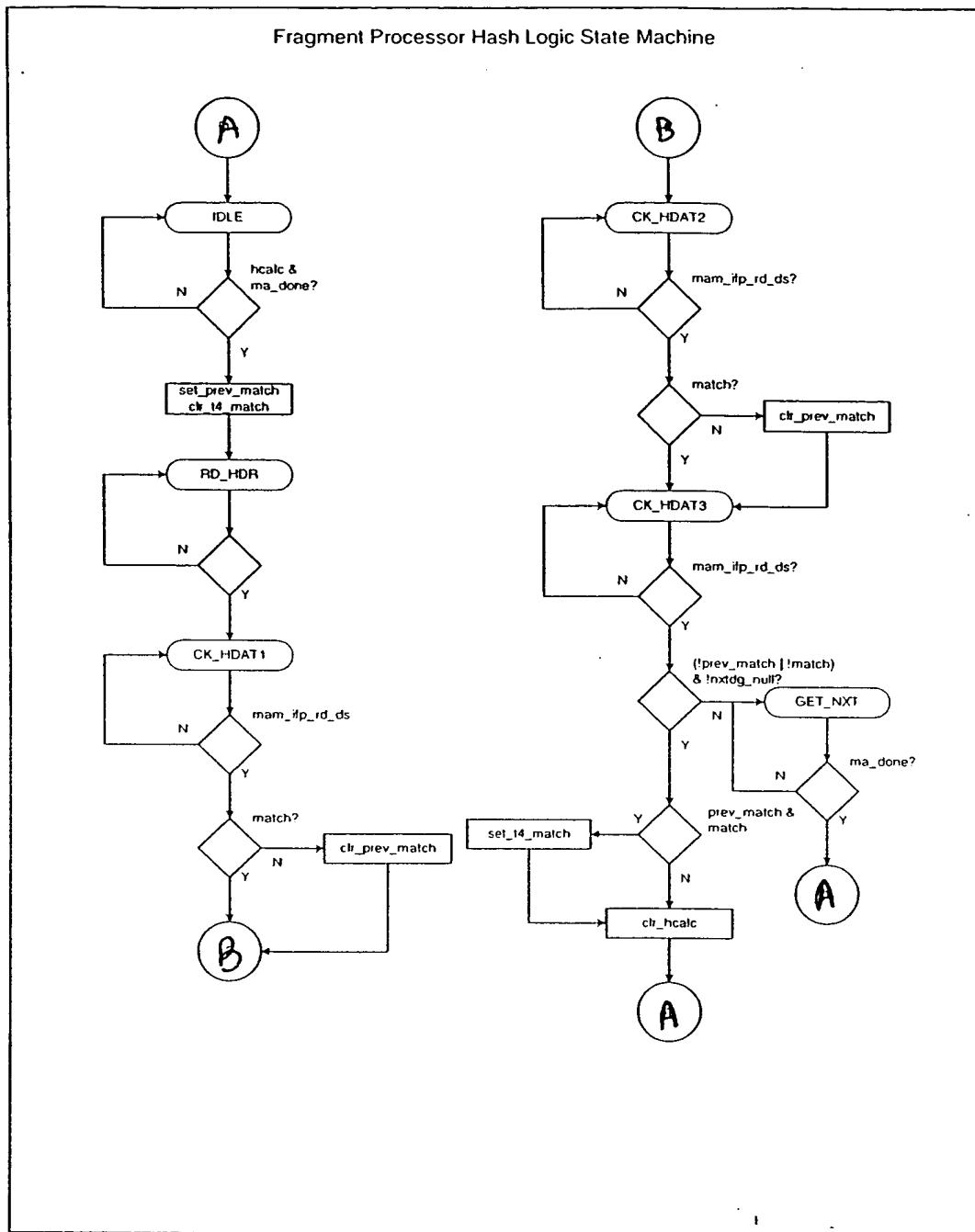


Figure 3L8 J

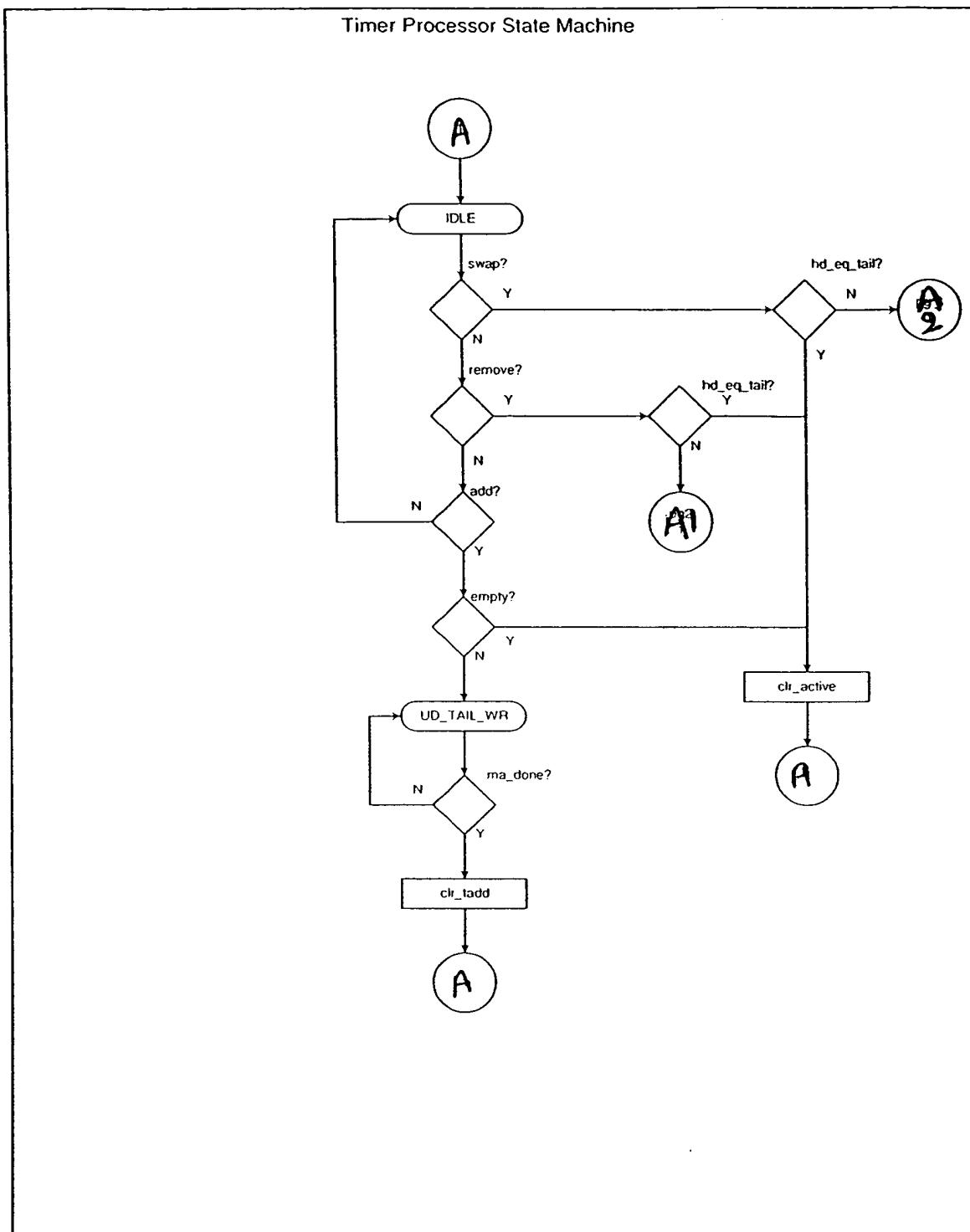


Figure 3L9A

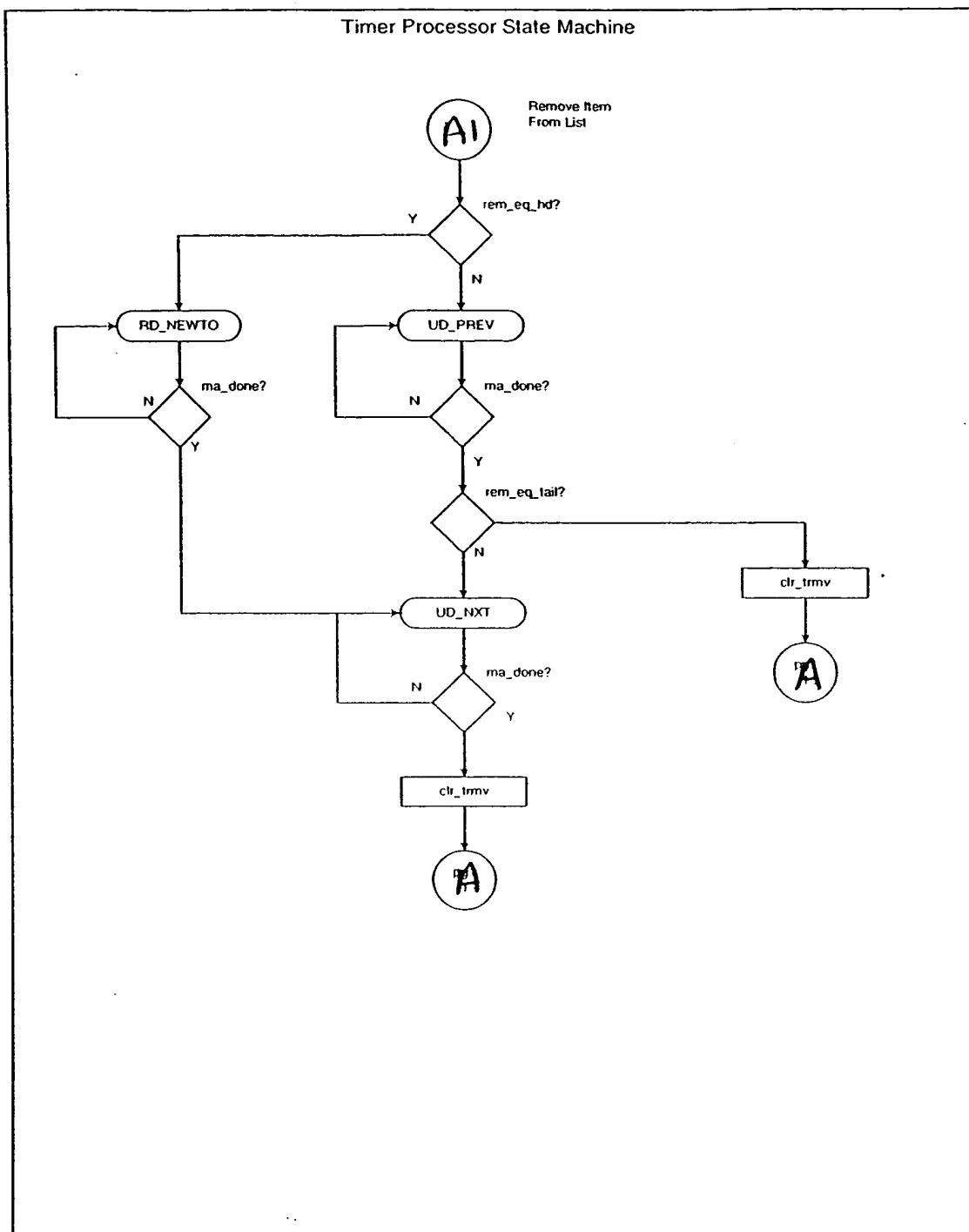


Figure 3L9B:

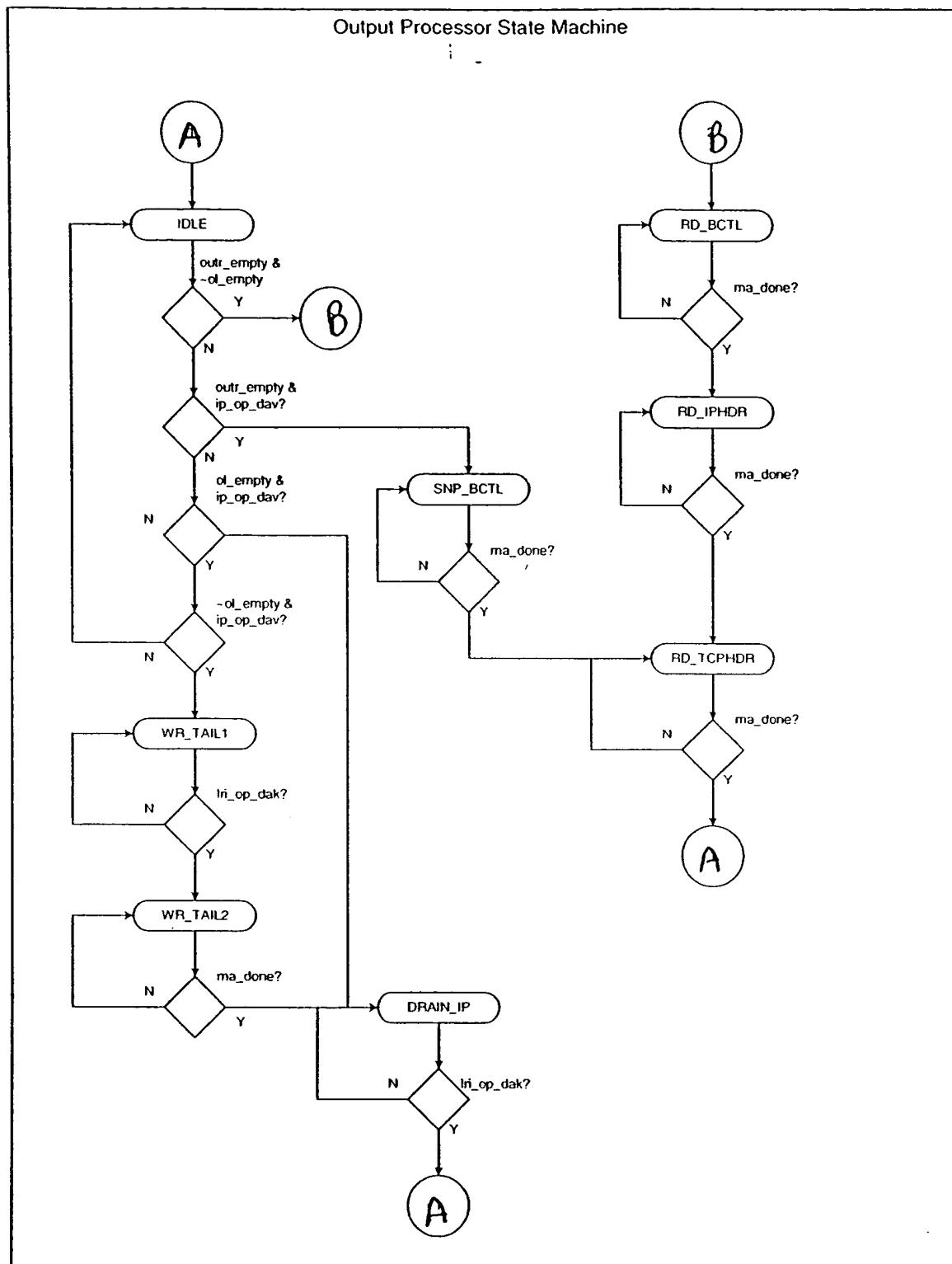


Figure 3L10 -

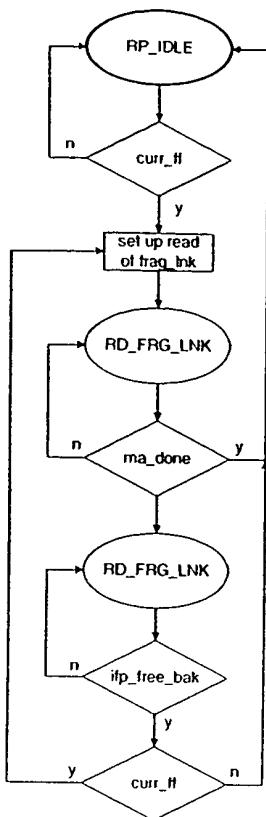


Figure 3L11 - Return Processor State Machine

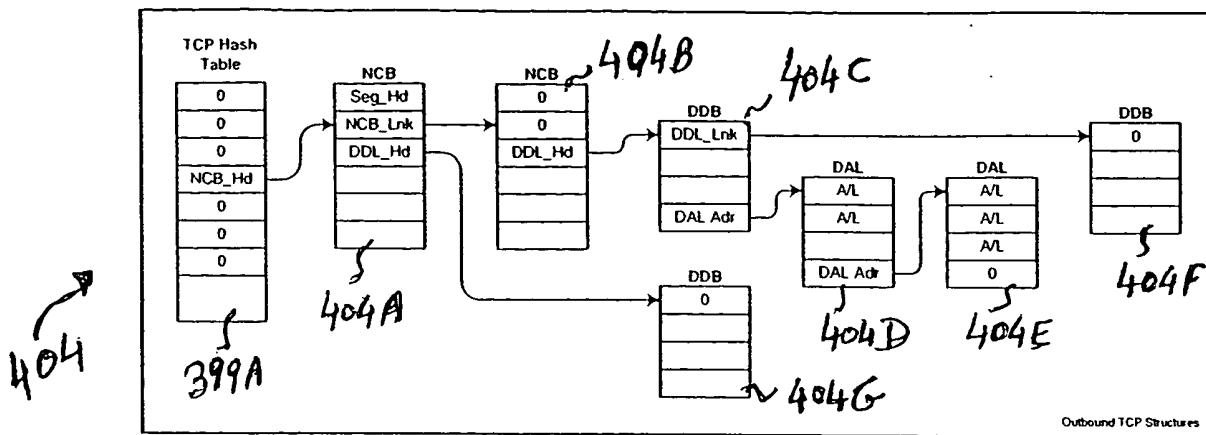


Figure 4A

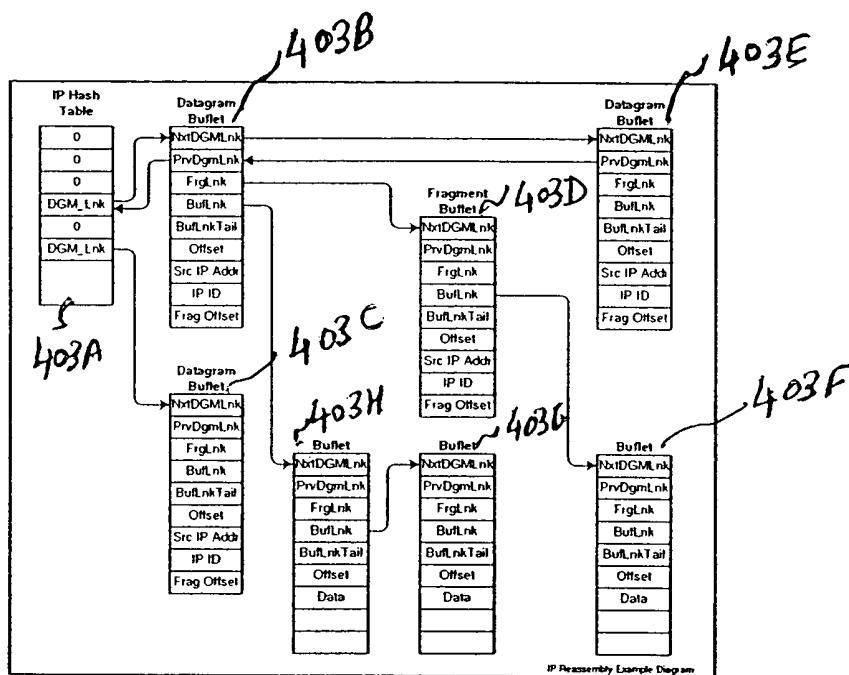


Figure 4B

403